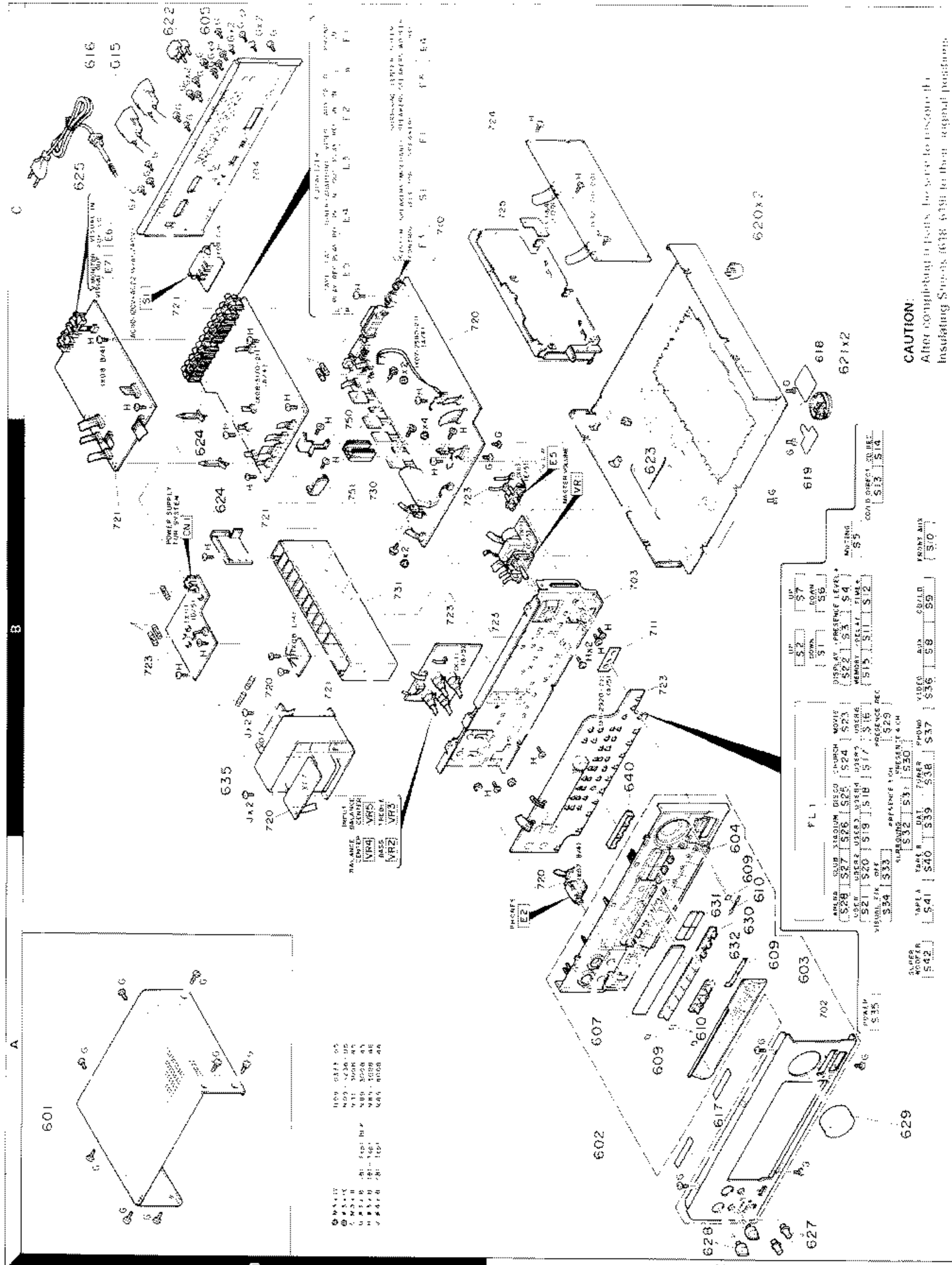


EXPLODED VIEW



CAUTION:
After completing repairs, be sure to restore the Insulating Sheets to their original positions.

Parts with the exploded numbers larger than 700 are not supplied.

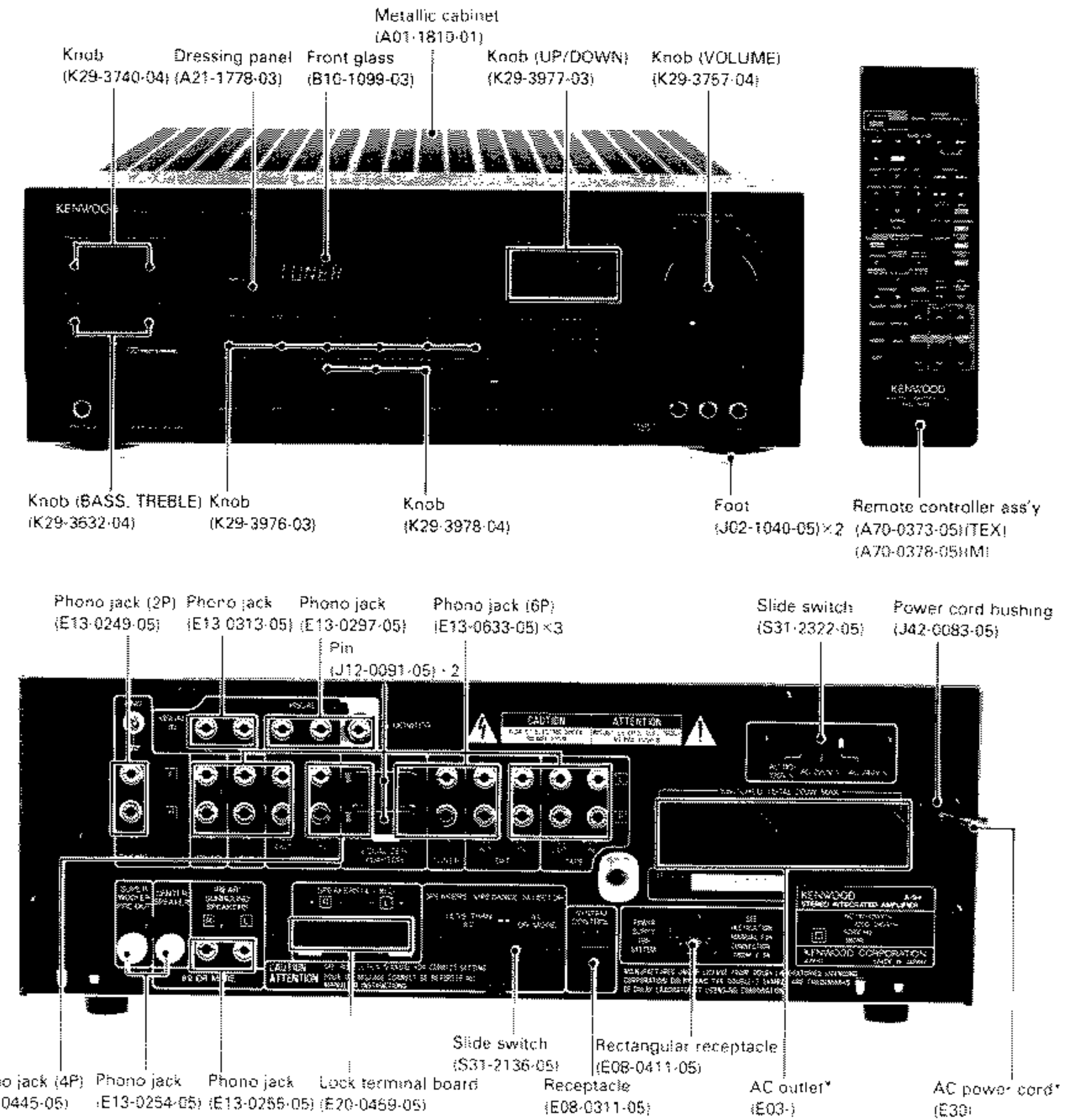
STEREO INTEGRATED AMPLIFIER

A-94

SERVICE MANUAL

KENWOOD

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B51-4187-00(S)1953



CAUTION:
After completing repairs, be sure to restore the Insulating Sheets to their original positions.

*Refer to parts list on page 42.

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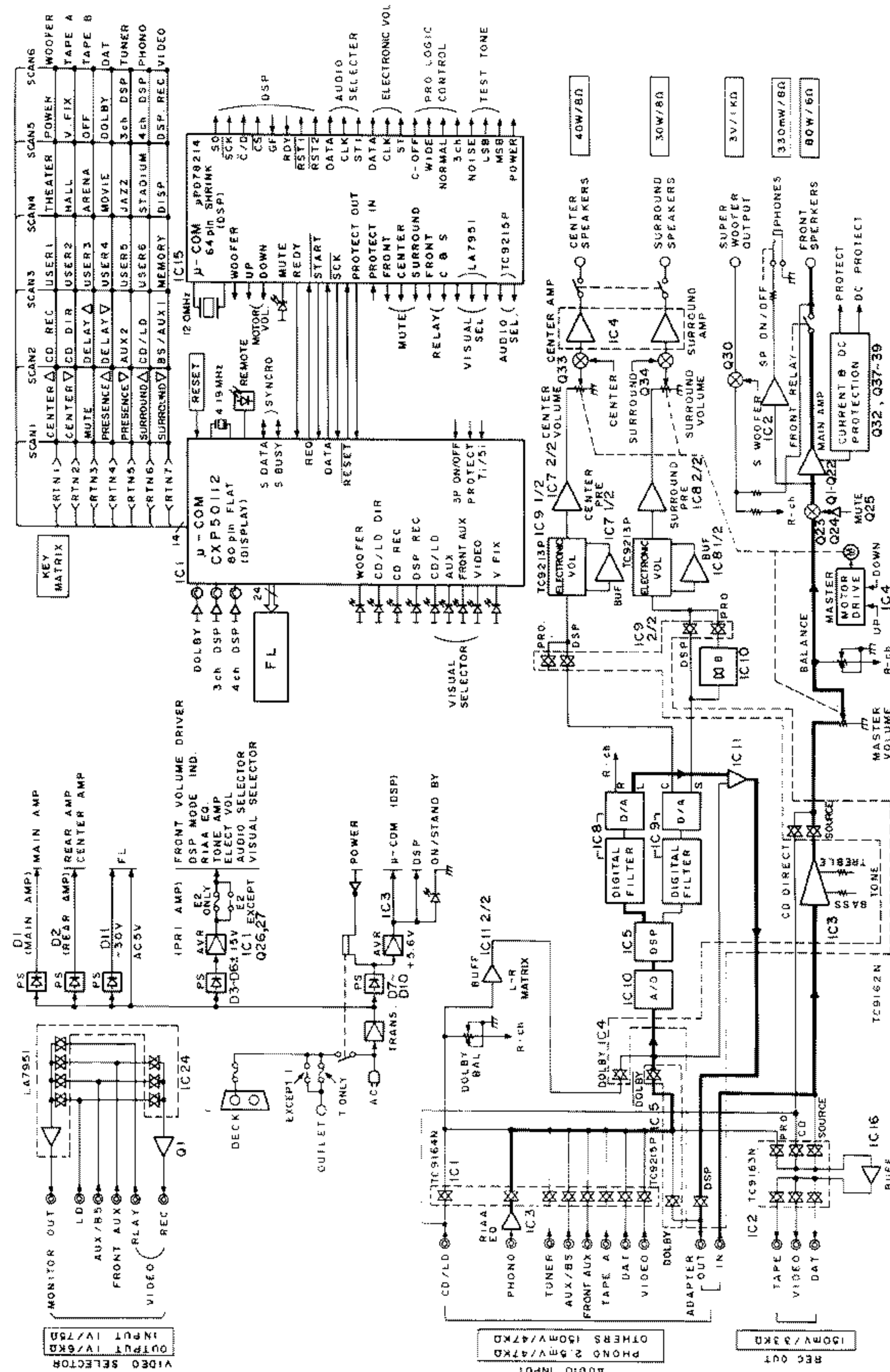
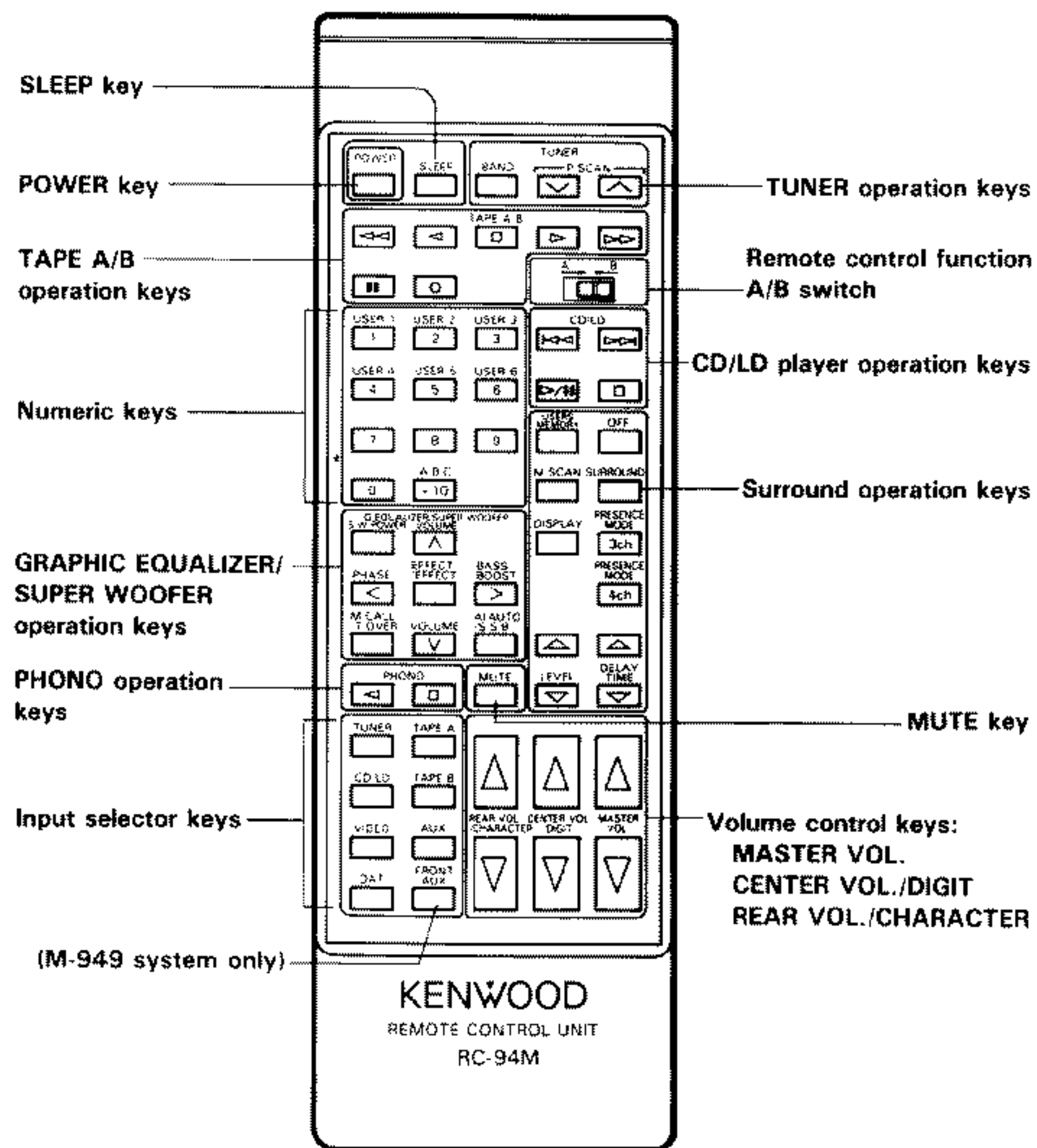
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BLOCK DIAGRAM

REMOTE CONTROL UNIT



3. PROCESSING USING DIGITAL SIGNAL PROCESSOR

The sound field characteristics of the hall described in the preceding section are realized with a digital signal processor as explained below.

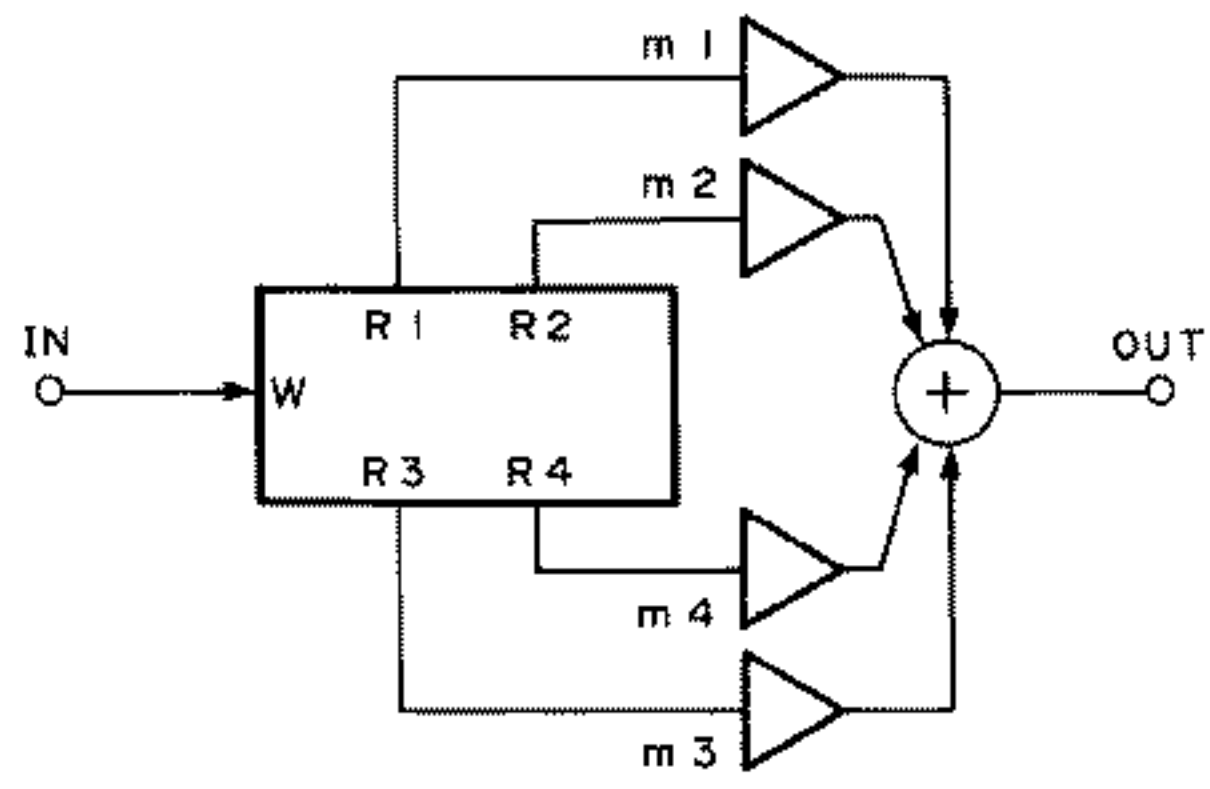


Fig. 10 Delay Device

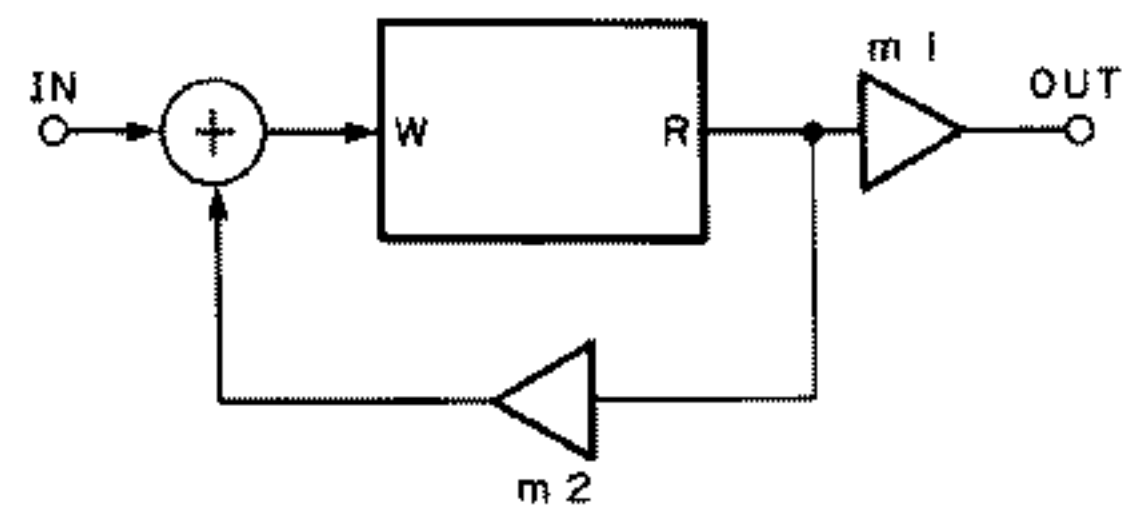


Fig. 11 Comb Filter

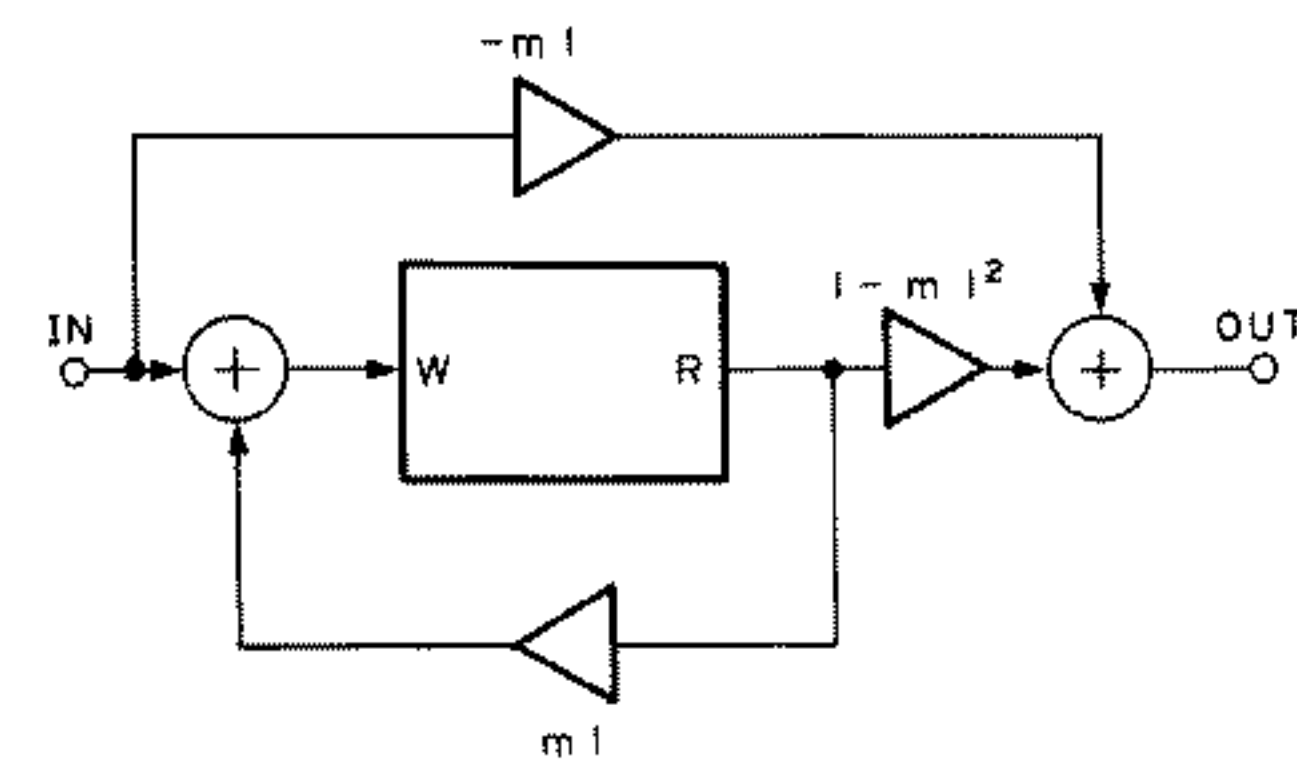
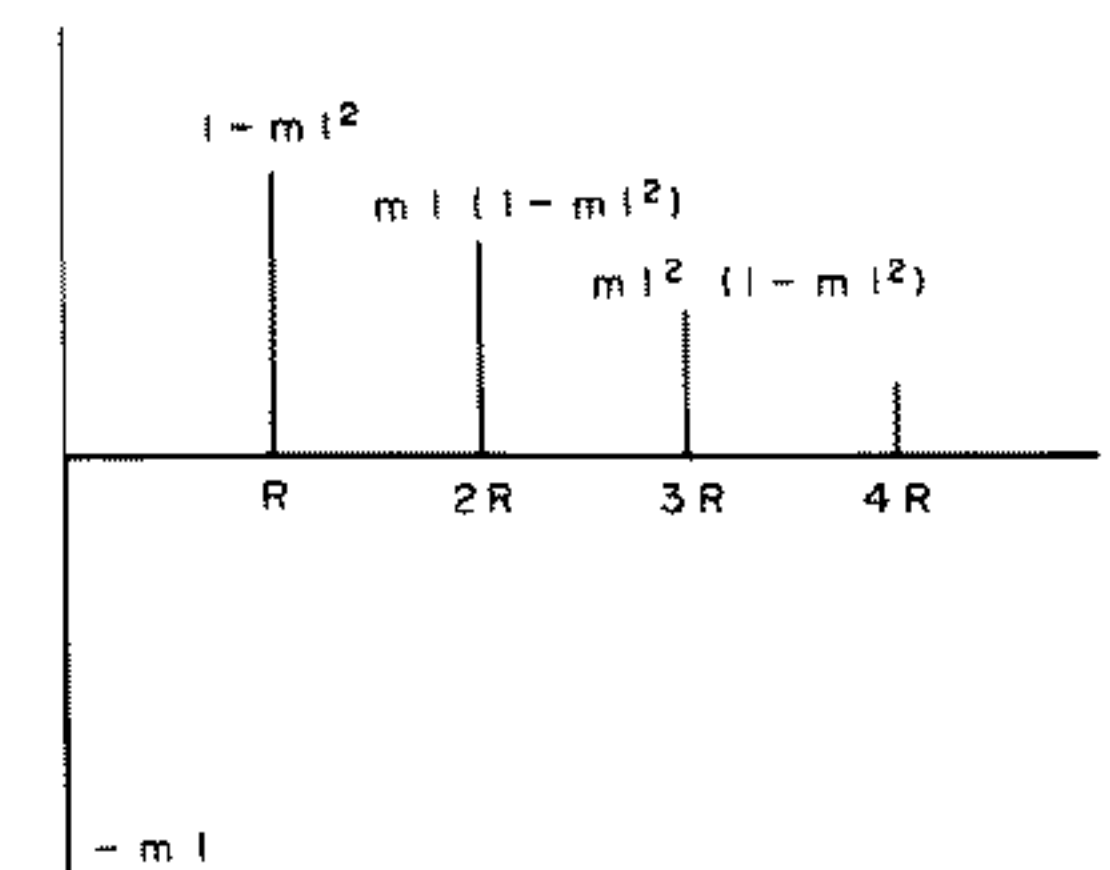
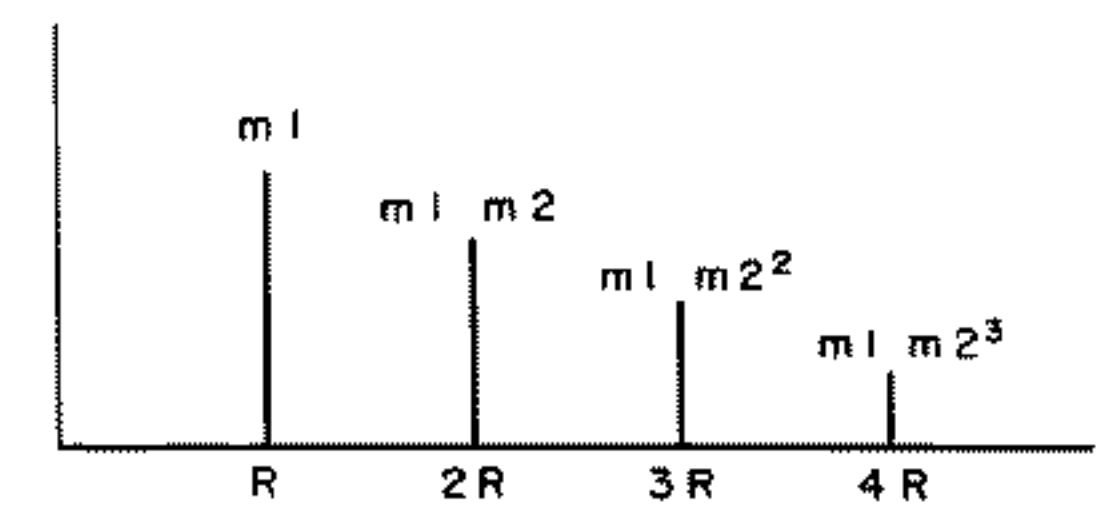
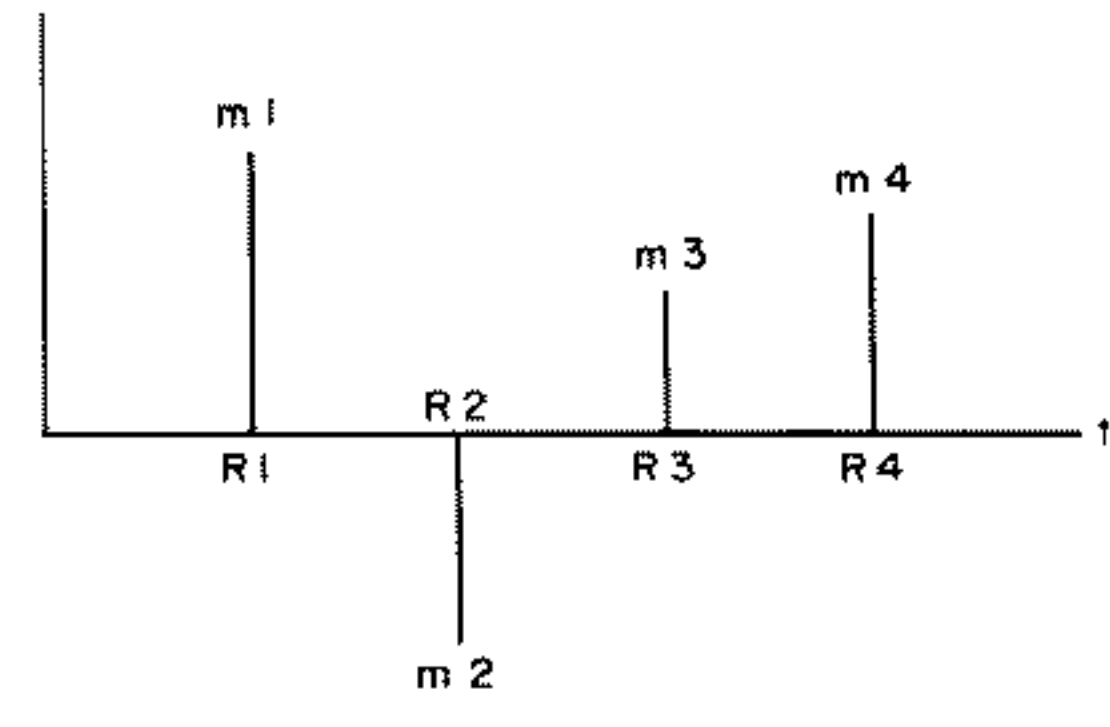


Fig. 12 All-Pass Filter

3-1 Initial Reflected Sound and Reverberated Sound

The basic components and echo patterns used to simulate the sound field of a hall are shown in Figures 10 through 12. Figure 10 shows the delay device that reproduces the initial reflected sound. A reverberated signal is reproduced using the reverberation system of a shredder. The reverberation system consists of a comb filter, shown in Figure 11, and an all-pass filter, shown in Figure 12.



3-2 Signal Processing Based on Signal Flow

Figure 13 shows a signal flow example when the sound field in a concert hall is reproduced with two front channels and two rear channels (4 channels in total) and four speakers based on the components in the preceding section. The input signal for the left and right channels branches into two paths. One is output to the front left and right channels as a direct sound. The other path is used to reproduce an indirect sound. The signal path in which the indirect sound is reproduced produces an L + R mono signal to simplify the processing. The indirect sound can also produce an attenuated high-frequency component characteristic by repeating the reflection and scattering on a wall, floor, or ceiling. In block 1, a low-

pass filter is thus constituted to eliminate the high-frequency components. In the signal flow shown in Figure 13, this signal processing is done by a primary IIR digital filter. The signal passing through the low-pass filter is input to initial reflected sound reproduction block 2. Three initial reflected sounds are then reproduced. Two initial reflected sounds are output to the front left and right channels. One initial reflected sound is input to block 3 to reproduce a reverberated sound. Block 3 uses the partially changed reverberation system of the shredder described in section 3-1. The reverberated sound is reproduced in block 3. The output signal of block 3 is input to block 4 and delayed relative to the front and rear left and right channels. The resultant signal is output to each channel.

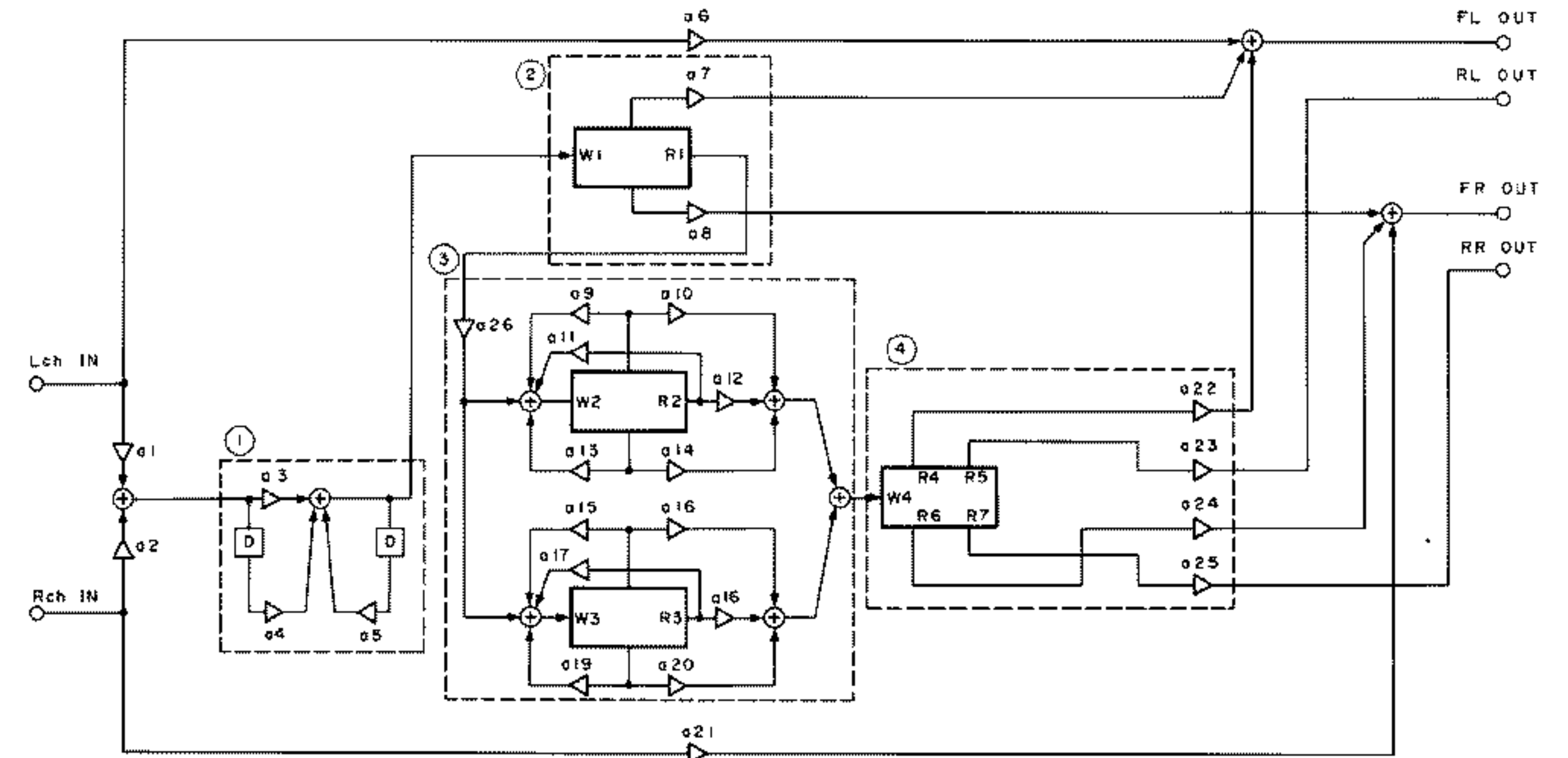


Fig. 13 Signal Flow in Concert Hall

During the above signal processing, various coefficients are used in each block in the course of signal flow. Various parameters such as reverberation time, low-pass filter cut-off frequency, and direct sound-to-indirect sound ratio, as well as initial

reflected sound and reverberated sound can be adjusted by altering these coefficients. These parameters can correspond to the environment used or the reproduced software information.

DSP CIRCUIT

1. INTRODUCTION

The digital signal processor (DSP) processes a digitized signal digitally to realize the desired functions. With the spread of digital audio, many audio digital signal processors have been developed recently. At present, the audio digital signal processor is primarily used for the following:

- 1) Digital filter (graphic equalizer and tone control)
- 2) Sound field control (hall sound creation)

The outline of the digital signal processor used for the sound field control in step 2 is described below.

2. ACTUAL SOUND FIELD PROPERTY AND ITS MODELING

2-1 Sound Field Component

The sound field peculiar to a concert hall consists of 1) a direct sound, 2) an initial reflected sound, and 3) a reverberated sound. The direct sound reaches the listening point directly from the sound source. The initial reflected sound reaches the listening point about 50 ms after being reflected off a wall or the ceiling several times. The reverberated sound is a higher-order reflected sound that is heard as a continuous sound without a sense of direction.

Figure 1 shows the conduction status of a reflected sound. Figure 2 shows the response at the listening point when a short sound is used as the sound source.

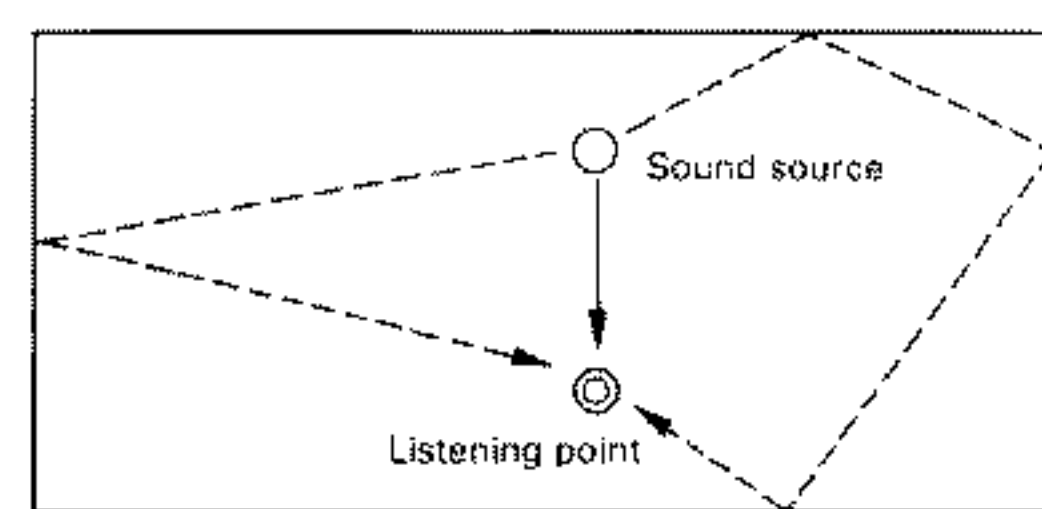


Fig. 1 Actual Sound Field

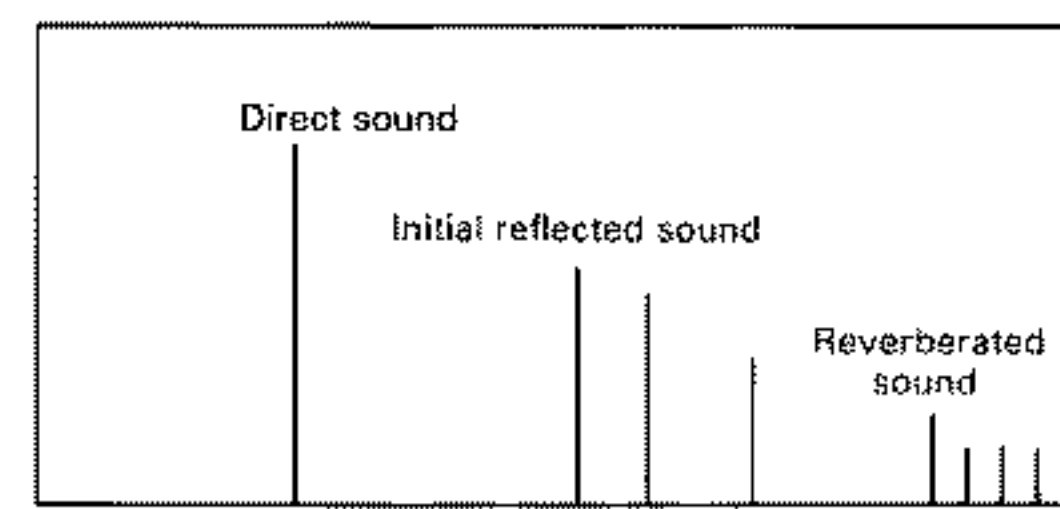


Fig. 2 Short-Sound Response

2-2 Actual Hall Sound

The actual sound characteristics of a concert hall are represented by an echo pattern (short-sound response) and directional pattern. The characteristic examples are shown in Figures 3 and 4.

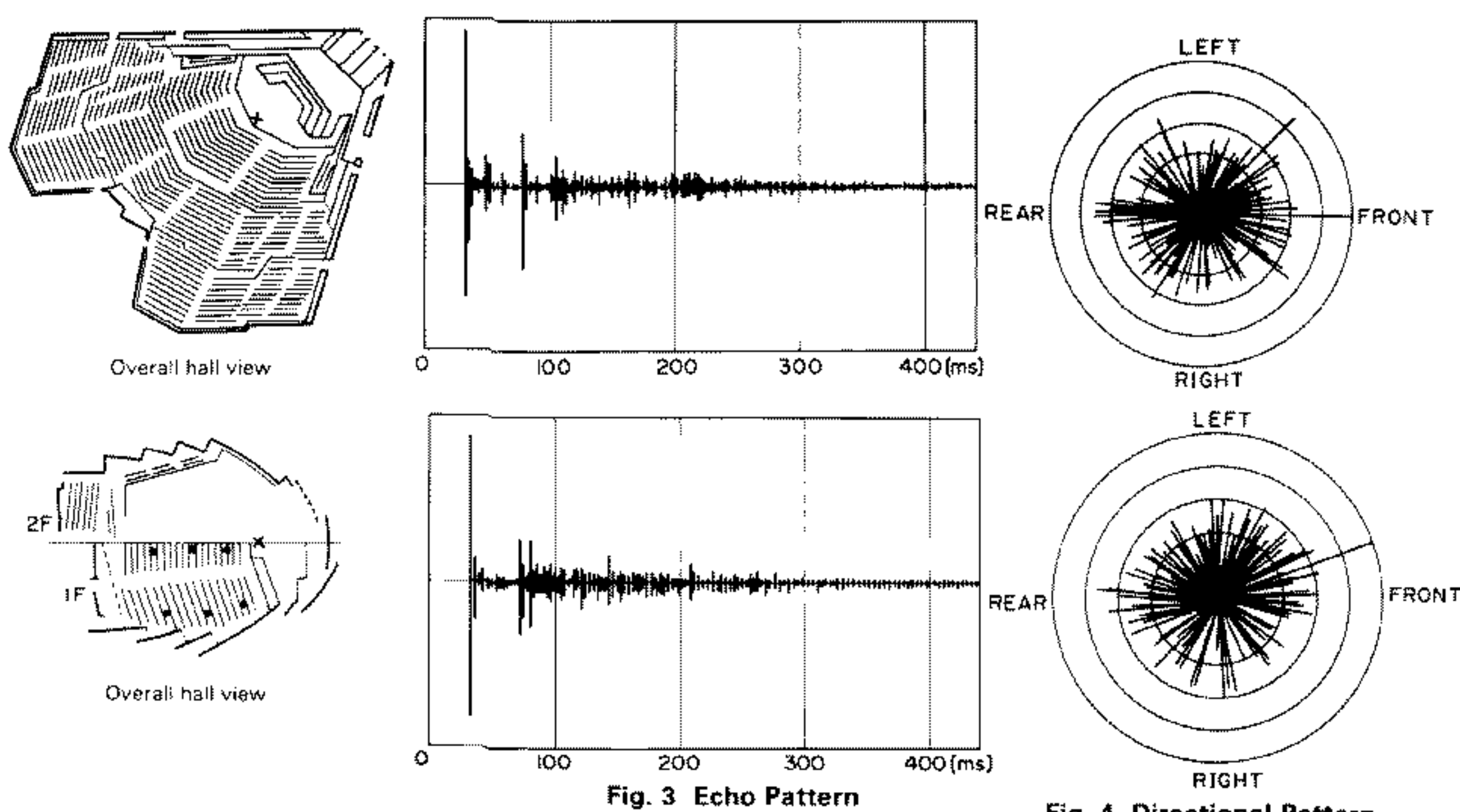


Fig. 3 Echo Pattern

Fig. 4 Directional Pattern

2-3 Sound Field Characteristic Modeling

To reproduce a hall sound with a digital signal processor, the sound field characteristic are modeled according to the functions of the digital signal processor, as described below.

- 1) Extract pulses characterizing the hall from the echo pattern shown in Figure 3.
- 2) Distribute the speakers used in accordance with the directional pattern shown in Figure 4.

Figures 5 and 6 show the modeled echo pattern and directional

pattern. The speaker arrangement when four channels are used and the echo patterns reproduced by each speaker are shown in Figures 7 and 8.

Only two or three front speakers may be used to control the sound field more easily. As a result, a wider sound field can be obtained by filtering a reverberated signal at the back and outputting the signal from the front. Figure 9 shows the sound field range in this case.

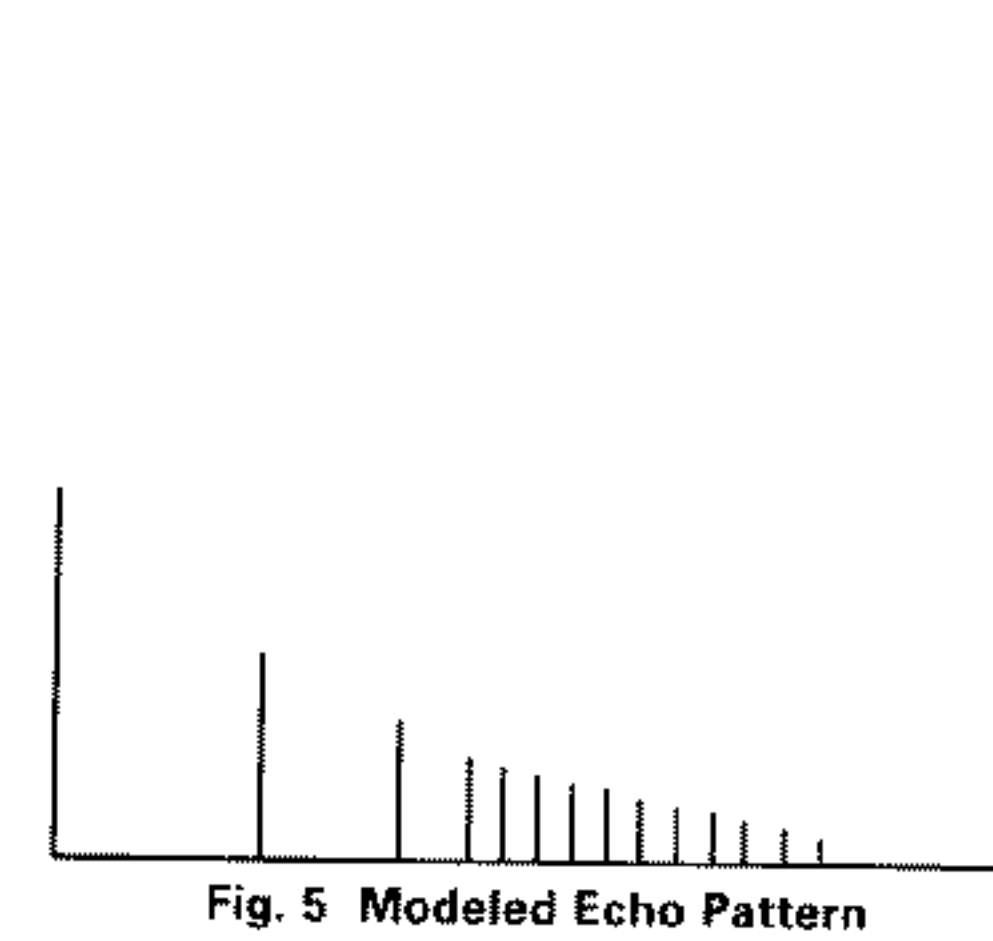


Fig. 5 Modeled Echo Pattern

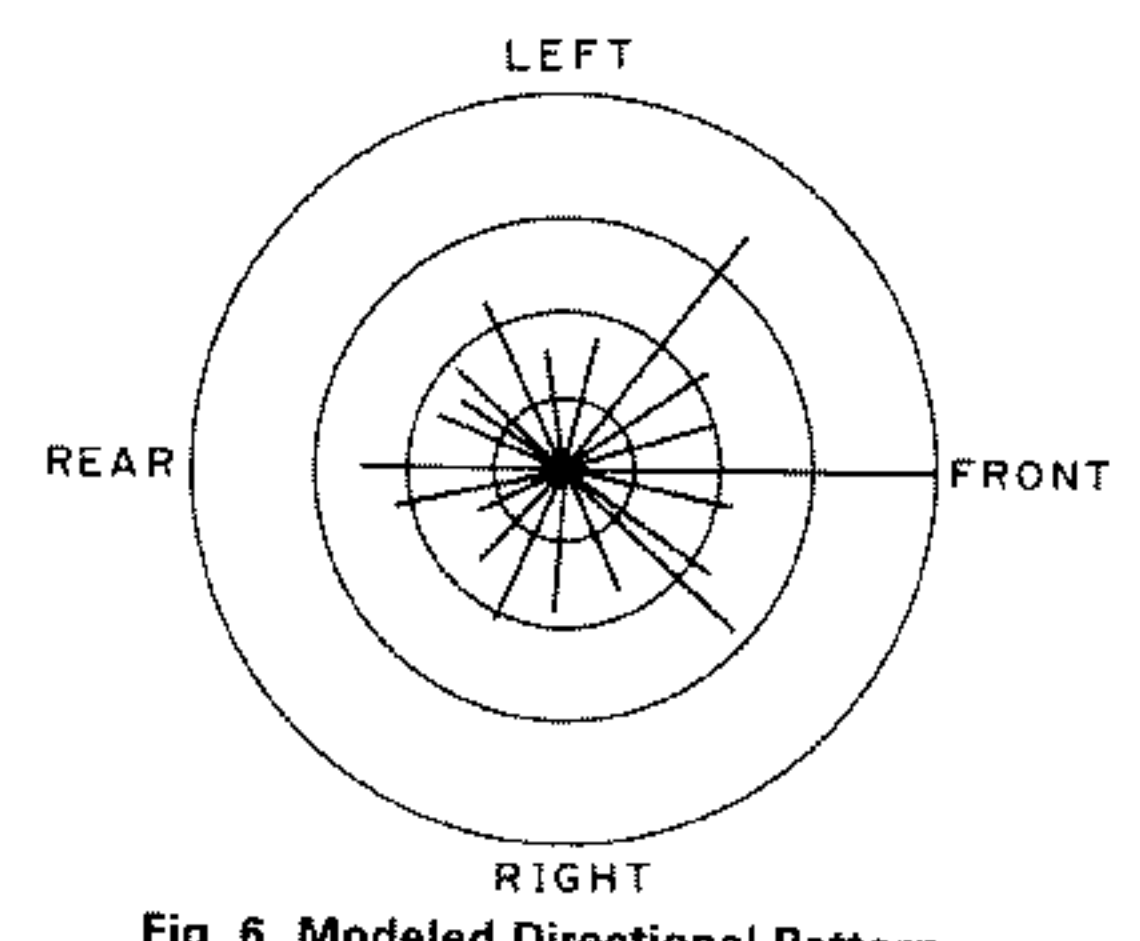


Fig. 6 Modeled Directional Pattern

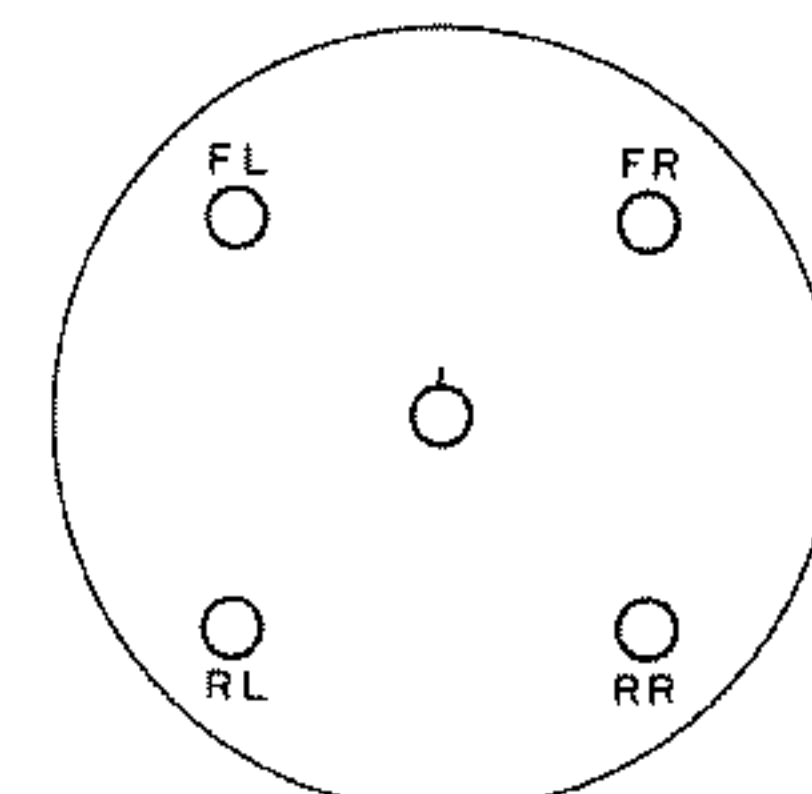


Fig. 7 Speaker Arrangement and Sound Field Range

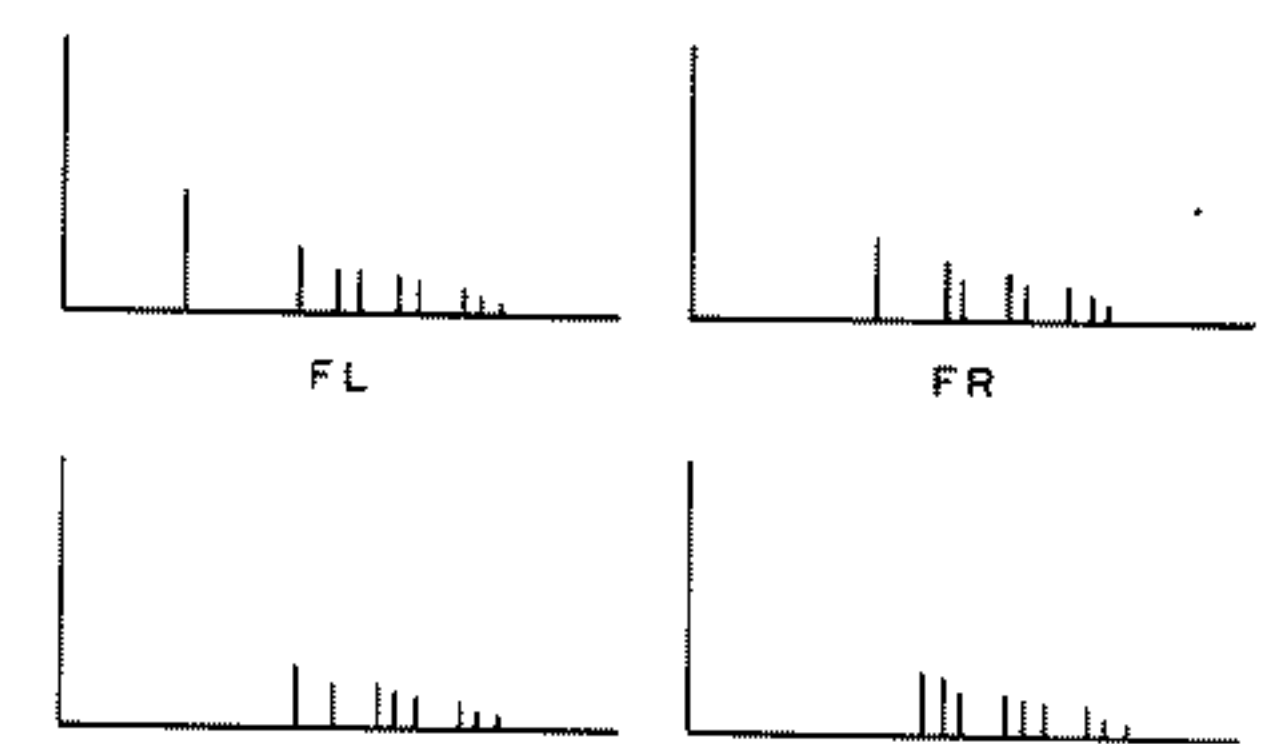


Fig. 8 Speaker Echo Patterns

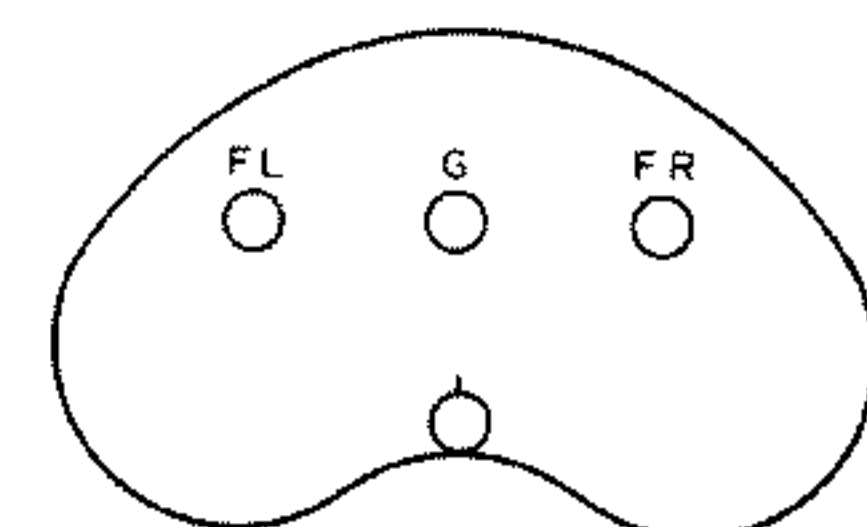
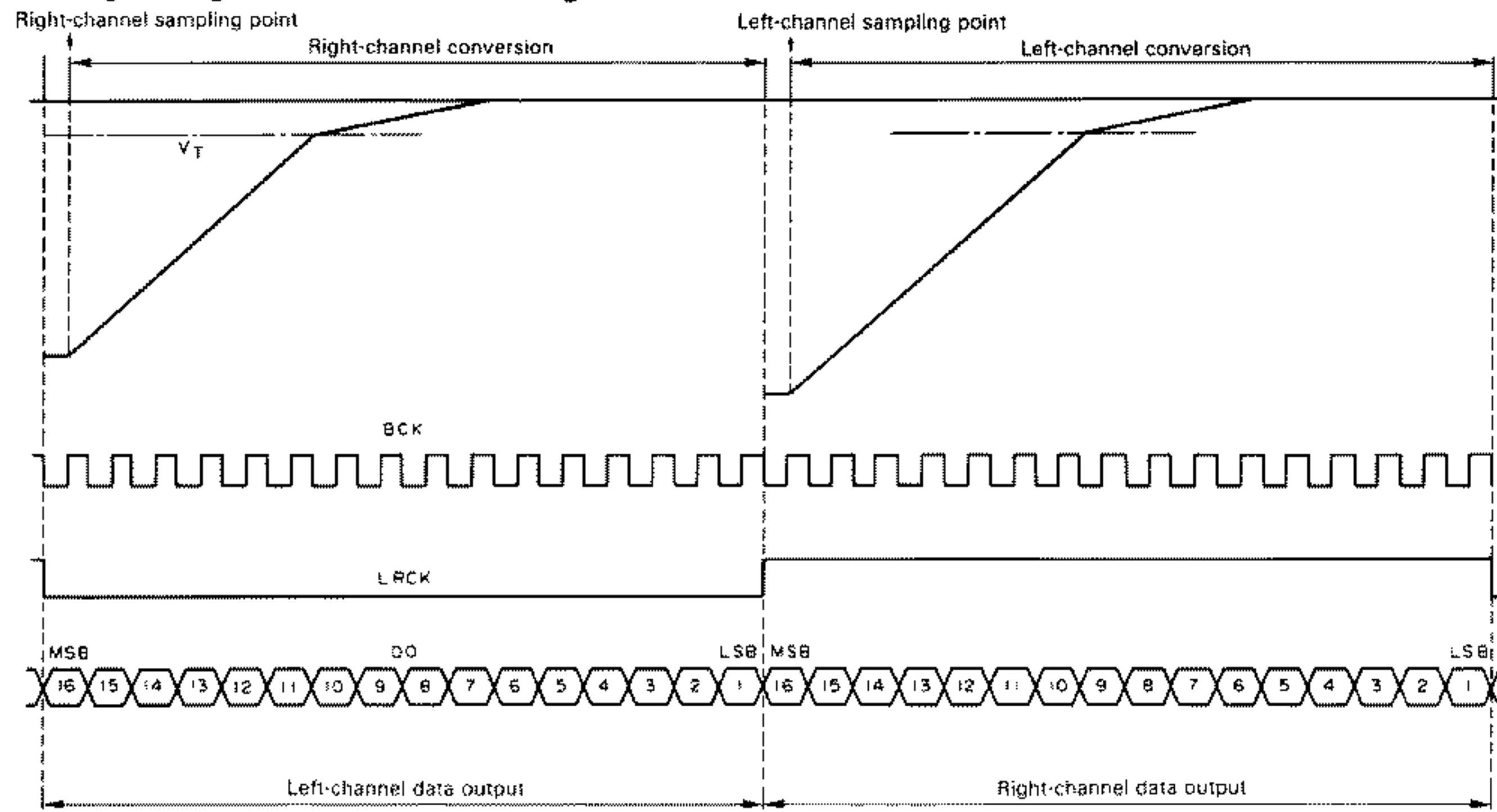


Fig. 9 Sound Field Range in Front Mode

CIRCUIT DESCRIPTION

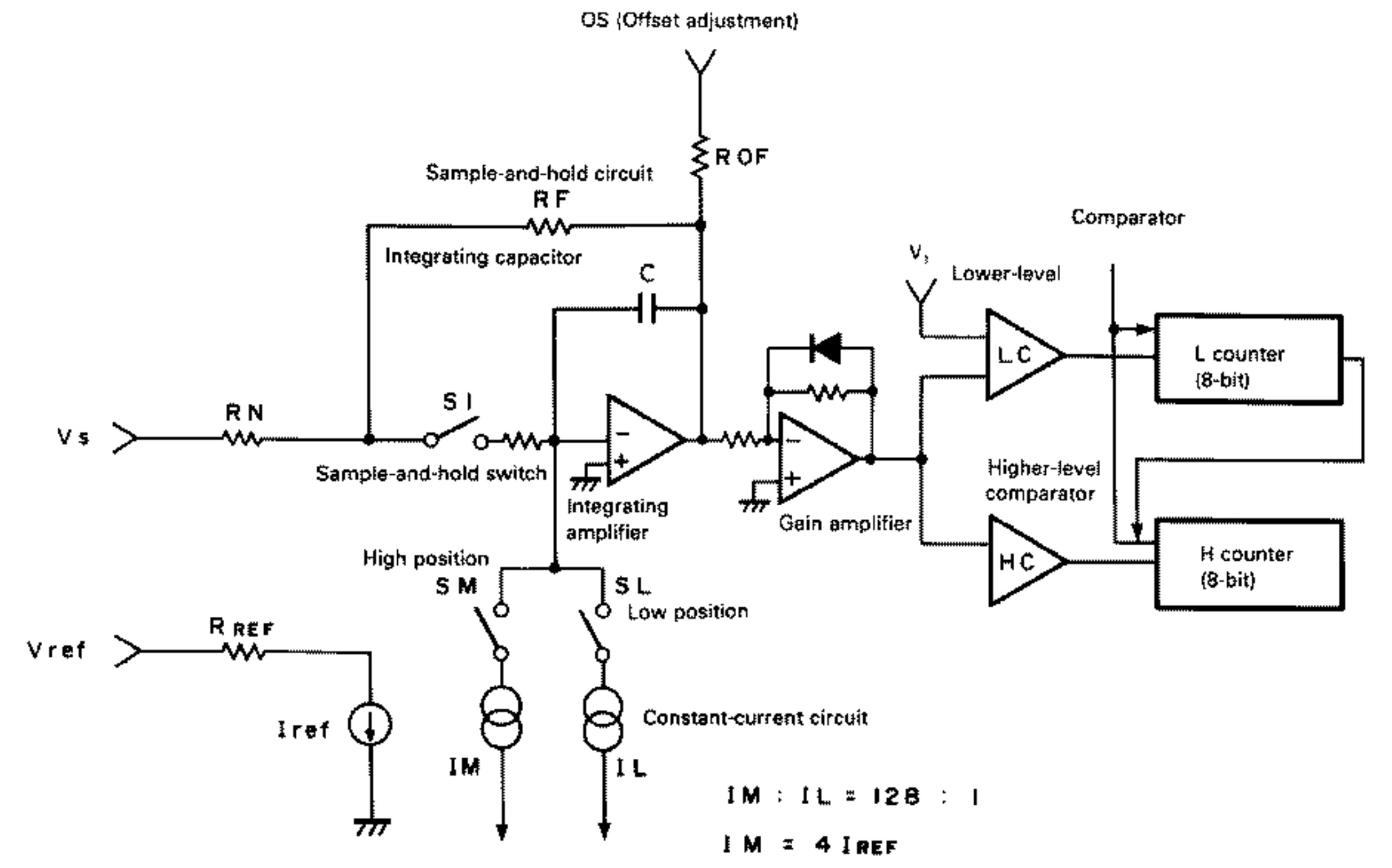
Pin No.	Symbol	I/O	Description	Remarks
13	VCCA	—	Analog positive supply voltage (+5 V)	
14	MSBL	O	Left-channel MSB signal output. (Output in offset binary codes.) MSB "1" → -1 V, "0" → +1 V	
15	VEEA	—	Analog negative supply voltage (-5 V)	
16	VTL	I	Left-channel comparator reference voltage	
17	AMPOL	O	Left-channel DC gain amplifier output	
18	AMPIL	I	Left-channel DC gain amplifier input	
19	AOUTL	O	Left-channel integrating amplifier output	
20	AINL	I	Left-channel integrating amplifier input	
21	SINL	I	Left-channel audio analog signal input	
22	GNDA	—	Analog ground	
23	IREF	I	Integrating reference current input	
24	GNDS	—	Analog signal ground	
25	SINR	I	Right-channel audio analog signal input	
26	AINR	I	Right-channel integrating amplifier input	
27	AOUTR	O	Right-channel integrating amplifier output	
28	AMPIR	I	Right-channel DC gain amplifier input	
29	AMPOR	O	Right-channel DC gain amplifier output	
30	VTR	I	Right-channel comparator reference voltage	

Analog-to-Digital Conversion Timing Chart



Basic Analog-to-Digital Conversion

Block Diagram

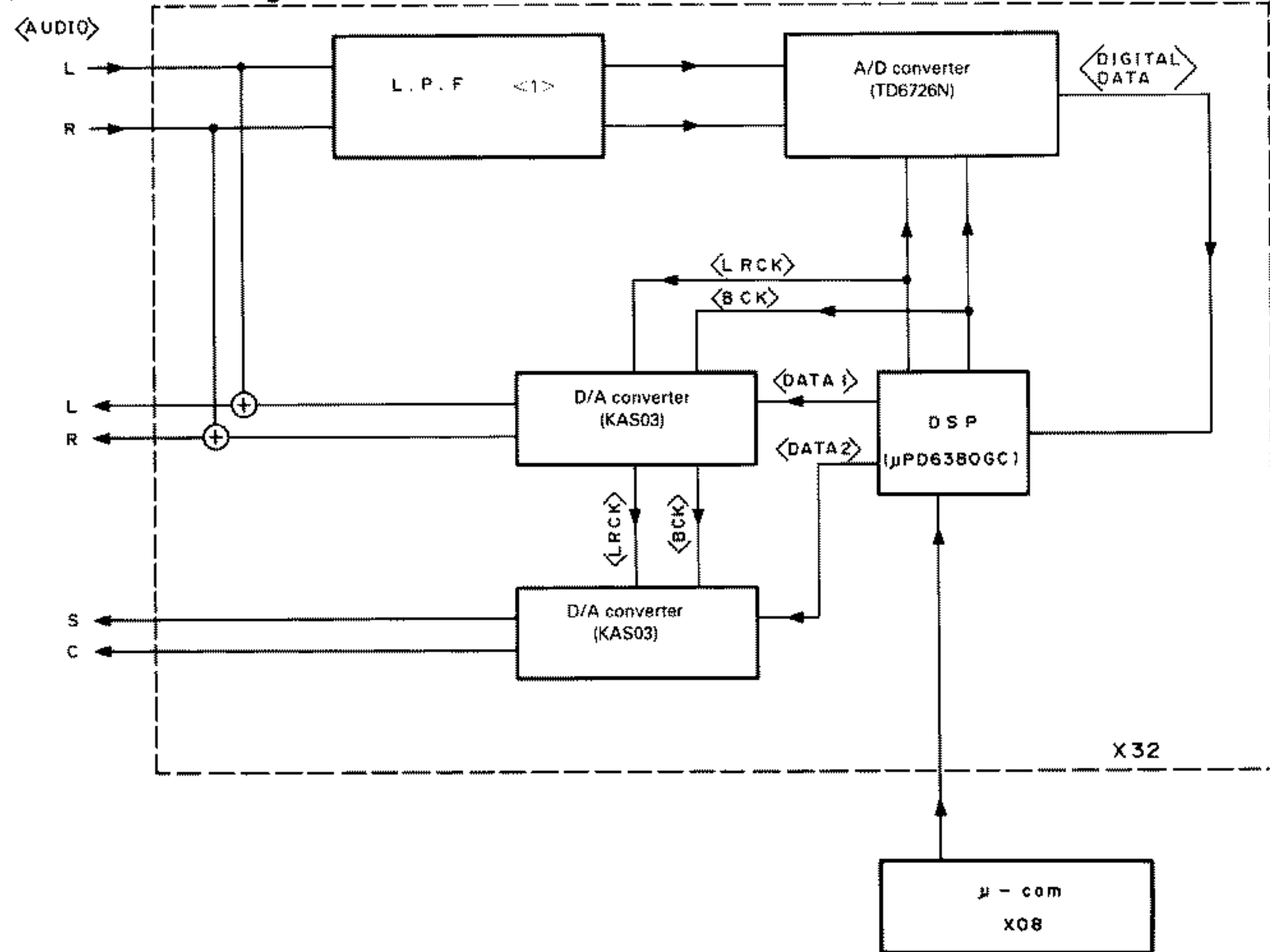


Basic Operation

S1	SM	SL	M counter	L counter	Description
1	On	Off	Off	Reset	(Sample) Holds the analog voltage applied to the Vs signal in a capacitor.
2	Off	Off	Count	Reset	(Hold) Turns off sample-and-hold switch S1 and holds the voltage just before the switch is turned off in a capacitor.
3	On	Off	Count	Count	(Conversion-1) Turns on the SM signal and discharges the capacitor with constant-current source IM. Turns off the SM signal when the capacitor voltage (VO) becomes zero. The higher-level M counter then counts the fck frequency.
4	Off	Off	On	Hold	(Conversion-2) Turns on the SL signal when the VO voltage becomes zero. Then discharges the capacitor with constant-current source IL. Turns off the SL signal when the VO voltage becomes VT. The lower-level L counter then counts the fck frequency. The L counter is carried to the M counter when a carry is produced by the L counter.
5	Off	Off	Hold	Hold	(Conversion completion) An A/D-converted digital data when the L and M counter information is concatenated is produced after all operations above are completed.

CIRCUIT DESCRIPTION

X32-1710 Block Diagram



(1) Block Description

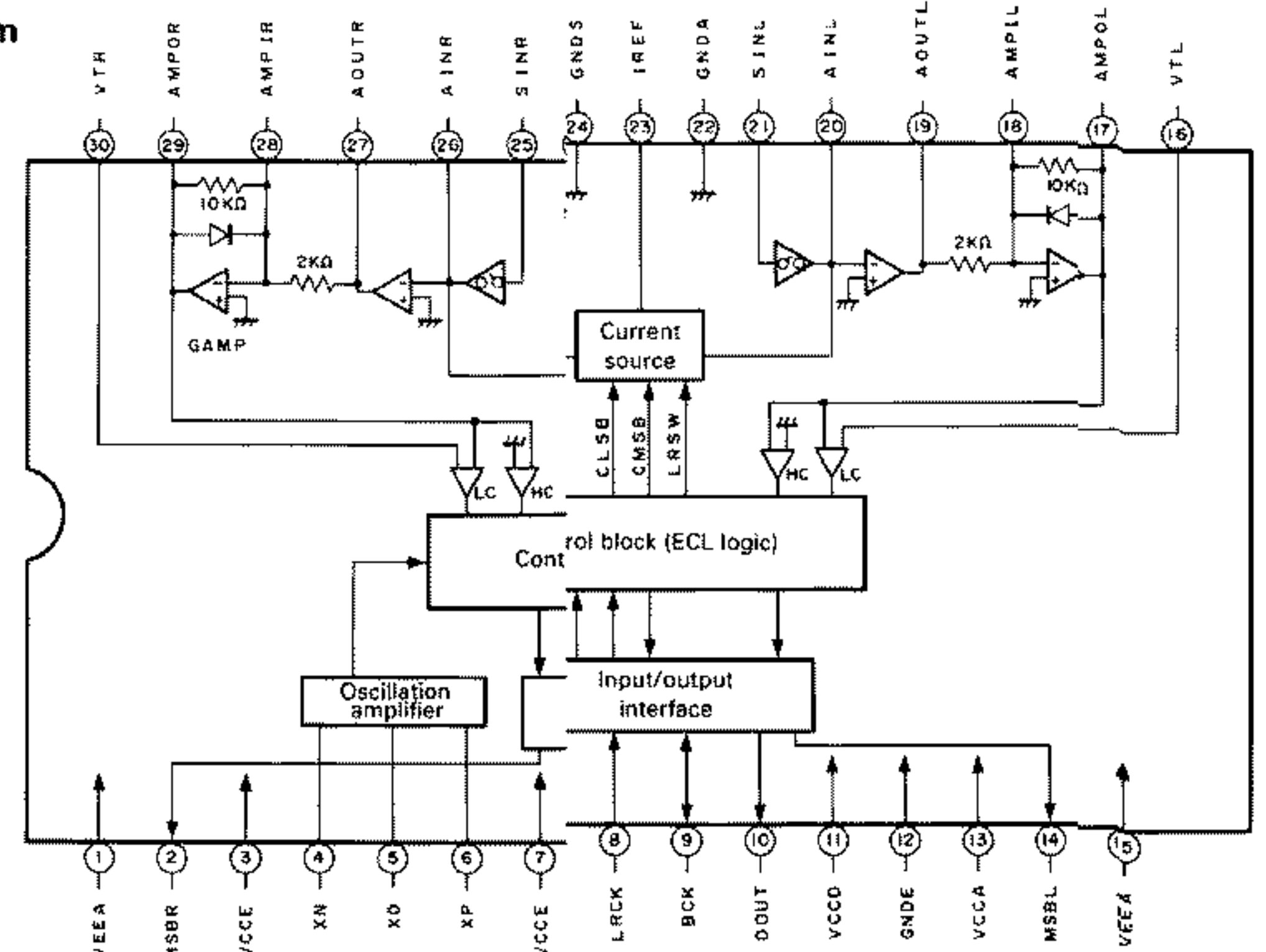
- (i) **Low-pass filter (LPF) <1>**
Cut-off frequency f_c is 15 kHz. The voltage level is -70 dB or more at 32 kHz to prevent cyclical noise in a 7-degree Tchebychev low-pass filter and analog-to-digital converter.
- (ii) **A/D converter (TD6726N)**
Sixteen-bit integrating analog-to-digital converter. This converter has an internal sample and hold circuit. The output is a 2's complement
- (iii) **D/A converter**
The digital-to-analog converter uses two KAS03s consisting of a hybrid integrated circuit (HIC), and has a 4-channel output.
The KAS03 includes a digital filter and 16-bit SH5807. The digital-to-analog converter includes a μ PD6376, and an +8 dB amplifier and low-pass filter (3-degree) that adjust the input and output gains of the X32-1710

(2) Operation

- Sampling frequency $f_s = 32$ kHz
 - A/D converter MCK = 67.7376 MHz
 - Digital signal processor MCK = 24.576 MHz
 - D/A converter MCK = 12.288 MHz
- The D/A converter MCK frequency is obtained by halving the 24.576 MHz frequency.
- (i) The front channel output signal is produced when the output signal for which the sound field is controlled by the digital signal processor and the through data are added.

CIRCUIT DESCRIPTION

Block Diagram



Pin Functions

Pin No.	Symbol	I/O	Description	Remarks
1	Δ VEEA	—	Analog negative supply voltage (-5 V)	
2	MSBR	O	Right-channel MSB signal output. (Output in offset binary codes.) MSB "1" → -1 V, "0" → +1 V	
3	VCCE	—	ECL logic positive supply voltage (+5 V)	
4	XN	I	Oscillation amplifier input/output.	
5	XO	O	A Colpitts oscillator can be easily configured by connecting a coil, capacitor, and resistor to the crystal oscillator.	
6	XP	I		
7	VCCD	—	ECL logic positive supply voltage (+5 V)	
8	LRCK	I	LR clock input	
9	BCK	I	Bit clock input	
10	DOUT	O	Digital audio data output. (Synchronized on the falling edge of the BCK signal for MSB data first-out operation.)	
11	VCCD	—	Digital positive supply voltage (+5 V)	
12	GNDE	—	ECL logic ground	

Master Microcomputer

Outline (CXP50112-154Q)

The main features of the A-94 display microcomputer are as follows:

- 2) Digital signal processor (DSP)
- 3) Digital signal processor (DSP) recording

- 1) 9-channel audio input and 4-channel video input

Initial Setting

Item	Setting	Item	Setting
POWER	OFF	Surround mode	OFF
Selector (audio)	TUNER	MEMORY	OFF
Selector (video)	VIDEO	VISUAL_FIX	OFF
CD DIRECT	OFF	CENTER	DOLBY -25 dB DSP -15 dB
MUTE	OFF	REAR	DOLBY -15 dB DSP -15 dB
CD REC	OFF	DEALAY TIME	DOLBY 20 mS DSP 35 mS
DSP REC	OFF	PRESENCE LEVEL	DSP only -8 dB
DISPLAY	Selector mode	DSP (30 ch, 4 ch)	3 CH ARENA 4 CH ARENA
SUPER WOOFER	OFF		

Memory Clear

Switch on while holding down the TUNER key.

Test mode

1. The amplifier test mode includes the two tests below
 - Test 1: Fluorescent display, LED, and lamp lighting test
 - Test 2: Surround test
 - a) Center level
 - b) Rear level
 - c) Presence level
 - d) Delay time
- 1) Selecting test mode
 - Switch on while holding down the CD key.
- 2) Use
 - 1) The fluorescent display and LED indicators light when the amplifier is switched on while the CD key is being held down. Lamps then light in the order of DOLBY, DSP3CH, and DSP4CH. Test 1 mode is entered.
 - 2) The amplifier enters test 2 mode when the keys below are pressed in test 1 mode. (Test 1 mode is canceled.)
 - a) CENTER UP KEY
 - b) REAR UP KEY
 - c) PRESENCE UP KEY
 - d) DELAY UP KEY

The CENTER, REAR, PRESENCE, and DELAY keys are activated cyclically as described below every time they are pressed.

Initial setting
Center level (-15 dB)
Rear level (-15 dB)
Presence level (-8 dB)

The surround mode is set to DSP4CH, and the presence mode to ARENA.
The surround mode is set to SURROUND when the DELAY key is pressed. The delay time is changed from 15 to 30 ms every millisecond (for about 1.5 seconds).

- * Keys other than CENTER, REAR, PRESENCE, and DELAY are accepted. However, the test mode may not operate normally when they are pressed.
- 3) Test mode canceling
Unplug the amplifier.

CAUTION:
When Test Mode is used, the memory will be cleared completely.

Timing Chart

16-Bit Communication Format

1. System

PPM two-wire bidirectional serial communication system

•Data configuration

The data configuration consists of 16 bits below.

- Model code 8 bits
- Function code 8 bits

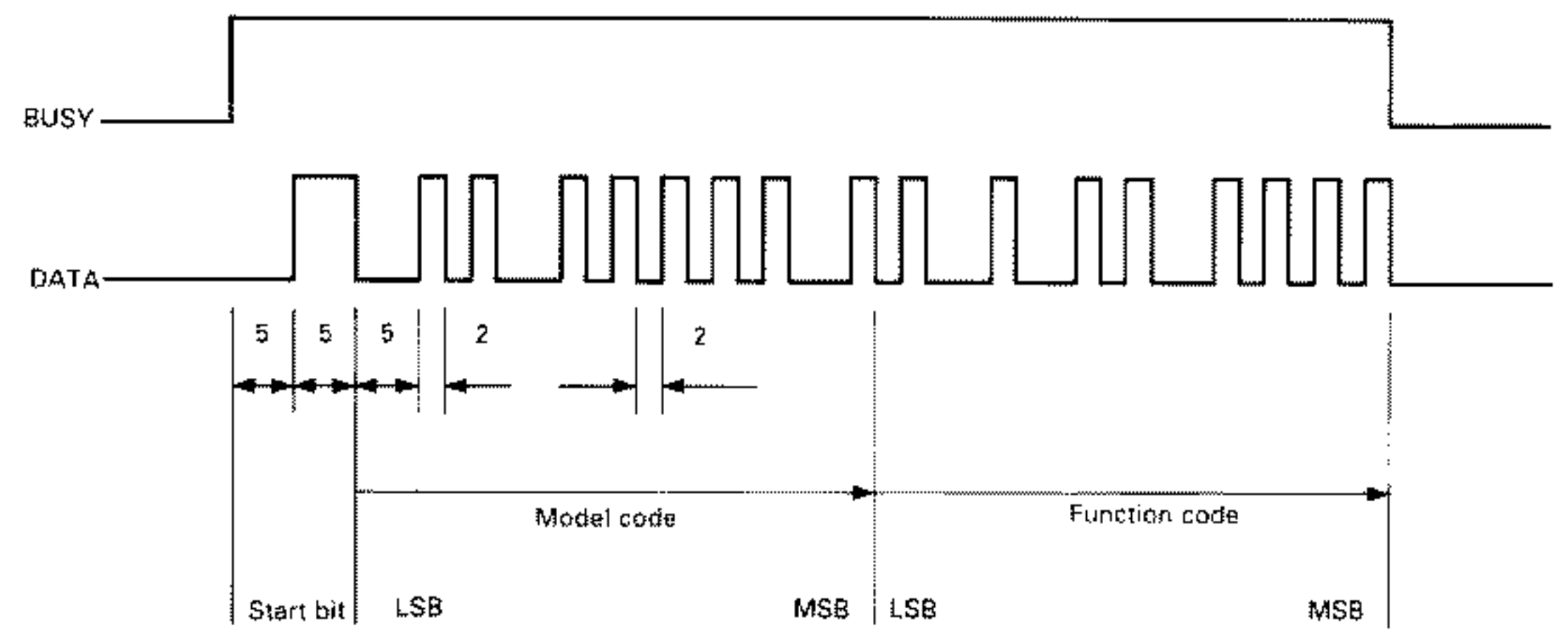
•Data format

•Start bit

Low: 5 ms, High: 5 ms

•Data

The binary value depends on the length of the space, which is delimited by 2-ms marks.
0: 2 ms, 1: 5 ms
In the example above, the model code is 85H and the function code is 16H



3-3 Internal Configuration and Operation of Digital Signal Processor

Figure 14 shows the internal block diagram of a digital signal processor (NEC μ PD6380). Each block operation is explained below.

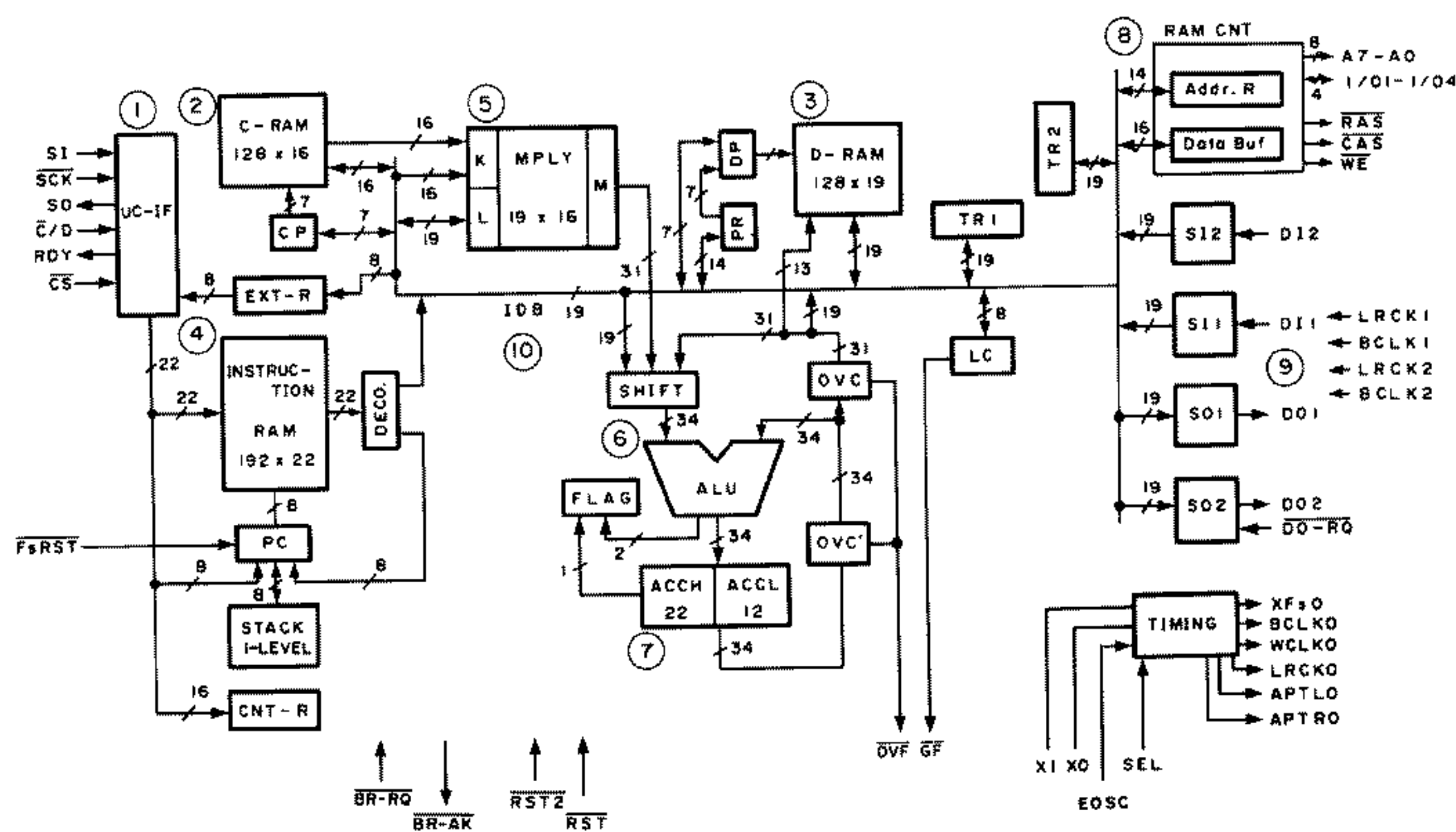


Fig. 14 Internal Block Diagram of Digital Signal Processor

- (1) Microcomputer interface Interfaces between a microcomputer and the digital signal processor in series.
- (2) Coefficient RAM (C-RAM) Stores the coefficient for a filter. The address is specified by a C-RAM pointer (CP).
- (3) Data RAM (D-RAM) The data RAM is a work area that stores data during operation. The address is specified by the D-RAM pointer (DP).
- (4) Instruction RAM Stores a program. The address is specified using a program counter (PC).
- (5) Multiplier (MPLY) The result data is sent to the M register in one instruction cycle when a multiplier and multiplicand are transferred to the K and L registers.
- (6) Arithmetic and logic unit (ALU) Performs arithmetic and logic operations.
- (7) Accumulator (ACC) The accumulator is a register that stores the result data of an ALU operation.
- (8) External RAM control Controls the external RAM in which delay data is written.
- (9) Audio interface inputs or outputs an audio signal in series.
- (10) Internal data bus (IDB) The internal data bus is an internal data transfer path between the memory and registers.

The process when the echo pattern shown in Figure 15 is reproduced by a digital signal processor is described below with the initial reflected sound as an example. See Figure 14, "Internal Block Diagram".

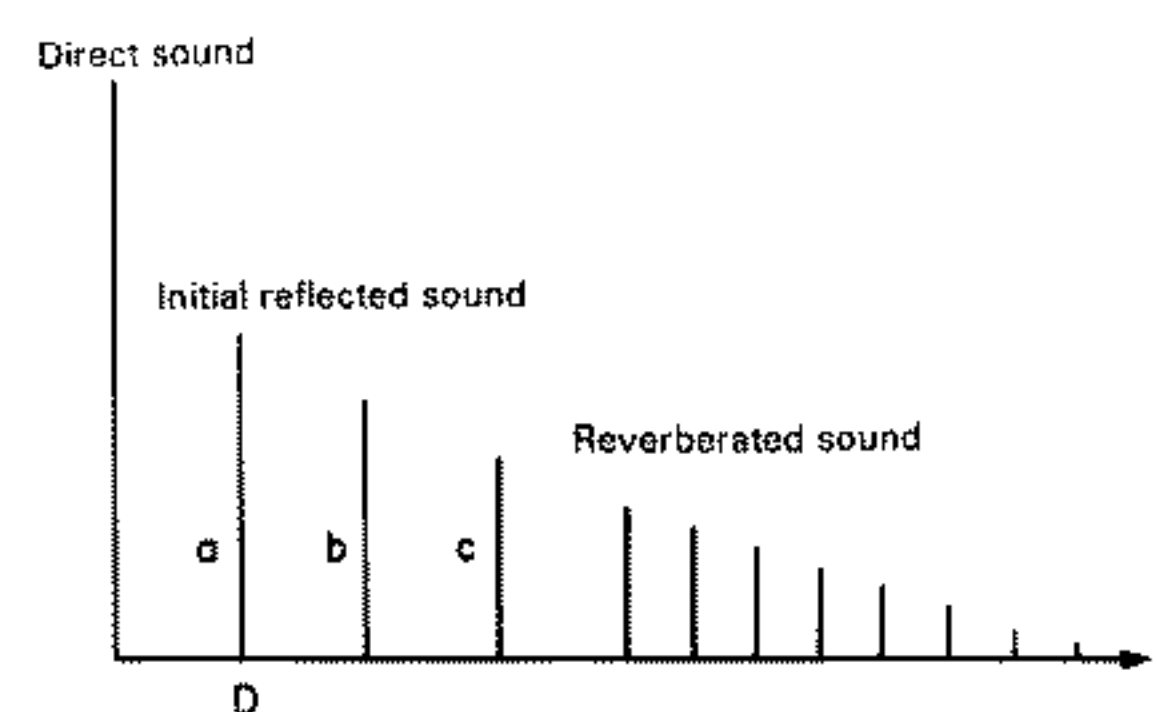


Fig. 15 Reproduced Echo Pattern

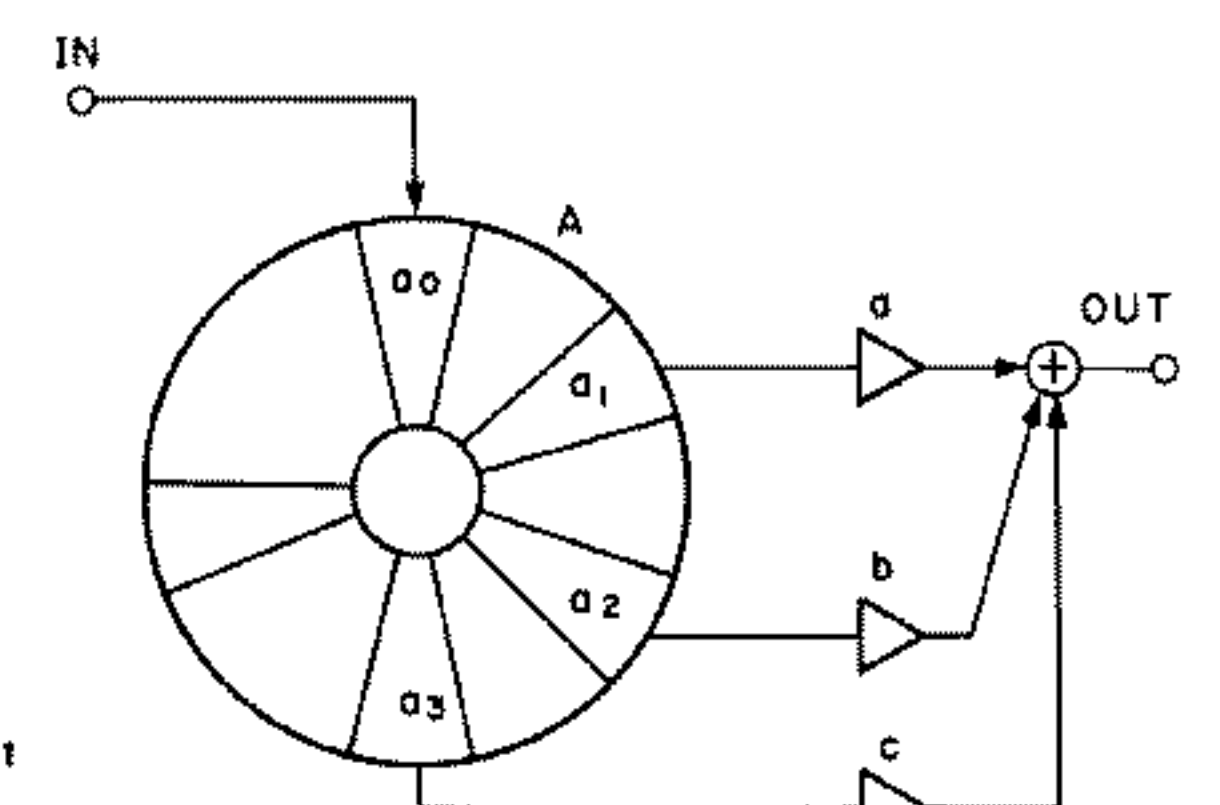


Fig. 16 Ring Buffer Model

The process when an initial reflected sound that is *a* times as high as the direct sound is reproduced after *D* seconds with the digital signal processor is as follows:

An audio signal is fetched from audio interface block 9 for every sampling period of *T* seconds and sent to external RAM control block 8. Data is then written in the external RAM constituting a ring buffer. Figure 16 shows the ring buffer example. An address is offset by $A = D/T$ relative to the currently written a_0 data and the a_1 data is read to obtain the delay time corresponding to *D* seconds. The read data is sent to the L register of multiplier 5. To make the data size *a* times the normal, coefficient *a* is sent from coefficient RAM 2 to the K register of the

multiplier. The multiplier performs a *K*, *L* operation in one instruction cycle. The result data is obtained using the M register. The signal that is delayed *D* seconds and made *a* times the normal size is sent from the M register to the arithmetic and logic unit (ALU) 6 and added to accumulator 7. The resultant signal is sent again from the accumulator to audio interface block 9. The signal is then output for every sampling period of *T* seconds. A subsequent initial reflected sound is also reproduced in the same way as above.

The digital signal processor performs the above signal processing at high speeds according to a horizontal instruction format. ALU operation, data transfer, and memory address updating are executed at the same time in one instruction cycle.

CIRCUIT DESCRIPTION

Pin Functions

Pin No.	Pin name	I/O	Name	Description
1	S4/DG0	O	SCAN4	Key scan 4
2	S5/DG1	O	SCAN5	Key scan 5
3	S6/DG2	O	DSP4CH	LAMP
4	S7/DG3	O	DSP3CH	LAMP
5	S8/DK0	O	SEGMENT 1	FL segment g
6	S9/DK1	O	SEGMENT 2	FL segment f
7	S10/DK2	O	SEGMENT 3	FL segment b
8	S11/DK3	O	SEGMENT 4	FL segment k
9	S12/PJ0	O	SEGMENT 5	FL segment i
10	S13/PJ1	O	SEGMENT 6	FL segment j
11	S14/PJ2	O	SEGMENT 7	FL segment a
12	S15/PJ3	O	SEGMENT 8	FL segment h
13	S16/T15	O	SEGMENT 9	FL segment c
14	S17/T14	O	SEGMENT 10	FL segment e
15	S18/T13	O	SEGMENT 11	FL segment n
16	S19/T12	O	SEGMENT 12	FL segment l
17	S20/T11	O	SEGMENT 13	FL segment m
18	S21/T10	O	SEGMENT 14	FL segment d
19	S22/T9	O	GRID 1	Fluorescent display grid 1
20	S23/T8	O	GRID 2	Fluorescent display grid 2
21	T7	O	GRID 3	Fluorescent display grid 3
22	T6	O	GRID 4	Fluorescent display grid 4
23	T5	O	GRID 5	Fluorescent display grid 5
24	T4	O	GRID 6	Fluorescent display grid 6
25	T3	O	GRID 7	Fluorescent display grid 7
26	T2	O	GRID 8	Fluorescent display grid 8
27	T1	O	GRID 9	Fluorescent display grid 9
28	T0	O	GRID 10	Fluorescent display grid 10
29	IMT	I	INT	Interrupt (unused)
30	TX	—	TX	32-kHz oscillator (unused)
31	TEX	I	TEX	32-kHz oscillator
32	RST	I/O	RST	Microcomputer reset
33	NC	—	—	—
34	V _{in}	—	V _{cc}	Power supply
35	PI0/AD0	I	7I/5I	Model discrimination
36	PI1/AD1	I	SP ON/OFF	Speaker on/off input
37	PI2/AD2	—	NC use	—
38	PI3/AD3	I	PROTECT	Protection input
39	PB0/AD4	I/O	DATA	Serial data line
40	PB1/AD5	I/O	BUSY	Serial busy
41	PB2/AD6	—	NC (OPEN)	—
42	PB3/AD7	O	DOLBY	LAMP
43	EC	—	NC (GND)	—
44	PX0/SC	O	SC	S = O clock (for communication)

CIRCUIT DESCRIPTION

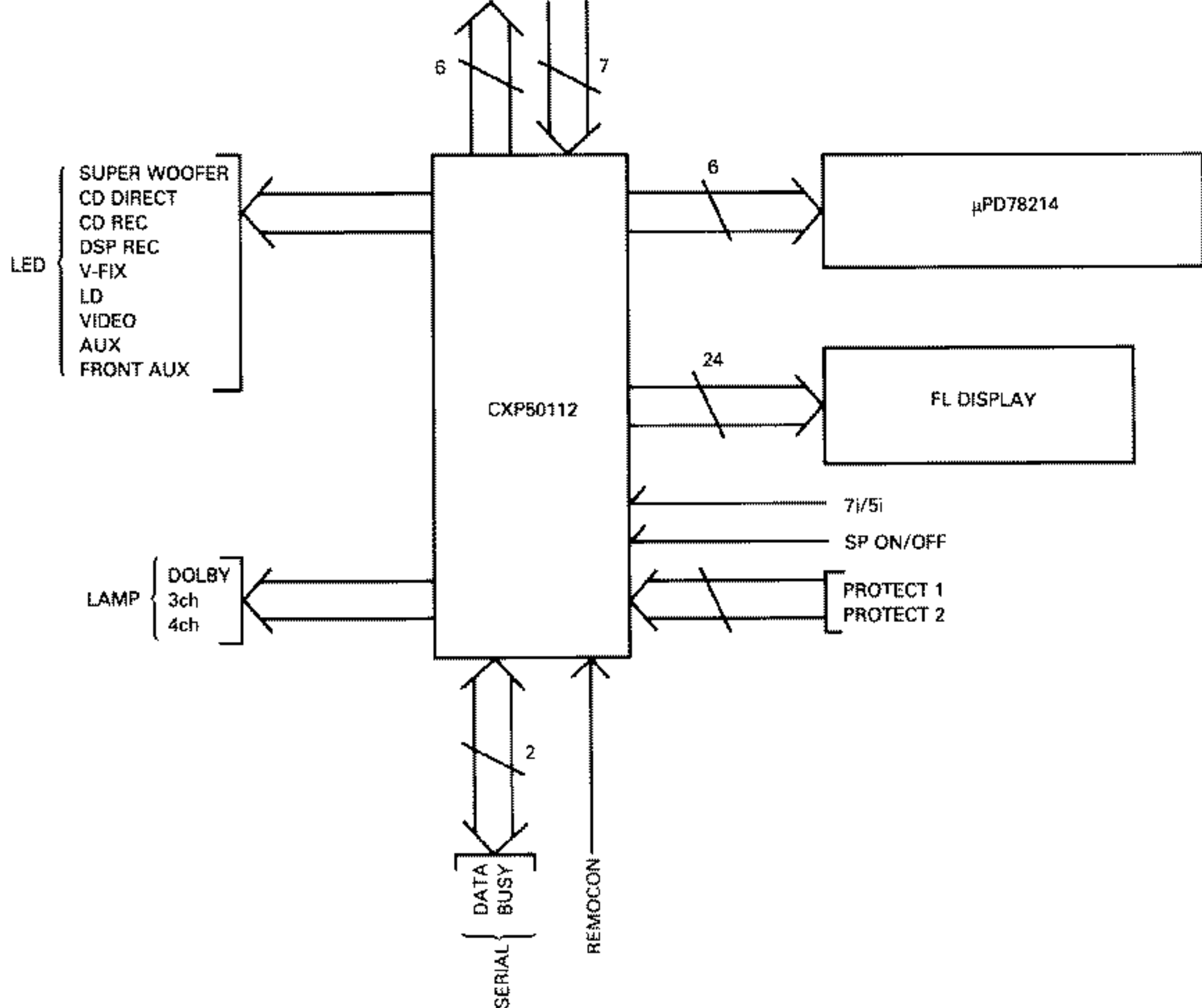
Pin No.	Pin name	I/O	Name	Description
45	PX1/SD	O	SD	S = O data (for communication)
46	PV2/S1	—	NC	NC (GND)
47	PA0	O	START	Start (for communication)
48	PA1	O	REQ	Request (for communication)
49	PA2	O	RESET	Reset (for communication)
50	PA3	I	READY	Ready (for communication)
51	PF0	O	SUPERWOOFER	LED
52	PF1	O	V-FIX	LED
53	PF2	O	CD-REC	LED
54	PF3	O	CD DIR	LED
55	PE0	O	F-FRONT AUX	LED
56	PE1	O	AUX	LED
57	PE2	O	CD/LD	LED
58	PE3	O	V2DEO	LED
59	PY0	O	DSP REC	LED
60	PY1/DWM	O	NC	NC (OPEN)
61	PY2/WP	I	BACK UP	Backup input
62	PY3/RMC	I	REMOCON	Remote control input
63	PD0	—	NC	NC (GND)
64	PD1	I	RETURN 0	Auto-return 0
65	PD2	I	RETURN 1	Auto-return 1
66	PD3	I	RETURN 2	Auto-return 2
67	PC0	I	RETURN 3	Auto-return 3
68	PC1	I	RETURN 4	Auto-return 4
69	PC2	I	RETURN 5	Auto-return 5
70	PC3	I	RETURN 6	Auto-return 6
71	V _{ss}	—	—	GND
72	XTAL	—	—	Crystal
73	NC	—	—	—
74	EXTAL	I	—	Oscillation pin
75	V _{sd} F	—	—	Reference voltage for voltage detection (unused)
76	V _{FP}	—	—	Fluorescent display power supply
77	PH0	O	SCAN 0	Key scan 0
78	PH1	O	SCAN 1	Key scan 1
79	PH2	O	SCAN 2	Key scan 2
80	PH3	O	SCAN 3	Key scan 3

CIRCUIT DESCRIPTION

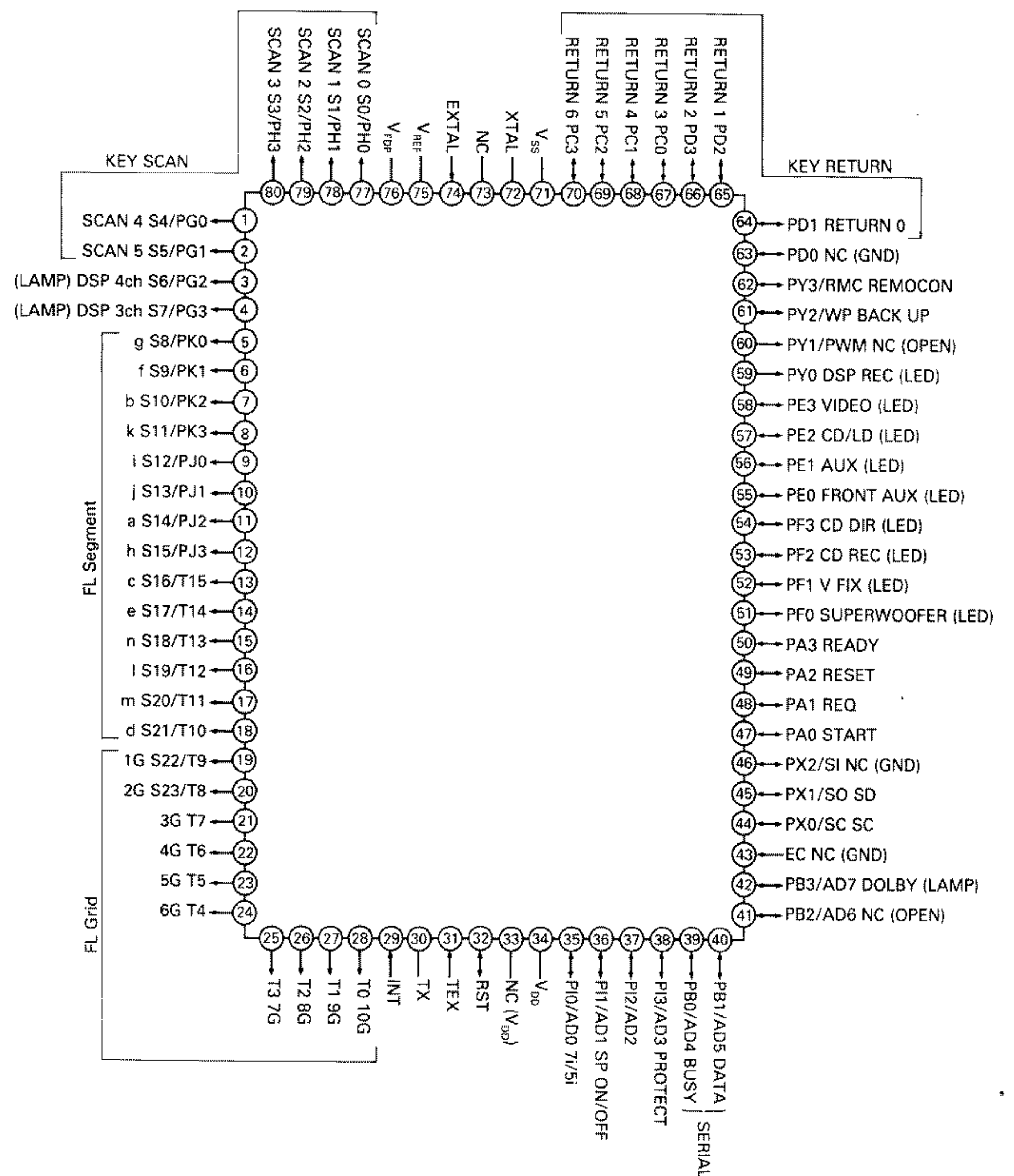
KEY SCAN

KEY RETURN

	0	1	2	3	4	5	6
CENTER	CENTER	MUTE	PRESENCE	PRESENCE	REAR	REAR	
CD REC	CD DIRECT	DELAY	DELAY	FRONT AUX	CD	AUX	
USER 1	USER 2	USER 3	USER 4	USER 5	USER 6	MEMORY	
ARENA	JAZZ CLUB	STADIUM	DISCO	CHURCH	MOVIE	DISPLAY	
POWER	V-FIX	OFF	DOLBY	DSP 3ch	DSP 4ch	DSP REC	
SUPER WOOFER	TAPE A	TAPE B	DAT	TUNER	PHONO	VIDEO	



CIRCUIT DESCRIPTION



CIRCUIT DESCRIPTION

Slave Microcomputer (μ PD78214CW-668)

Outline

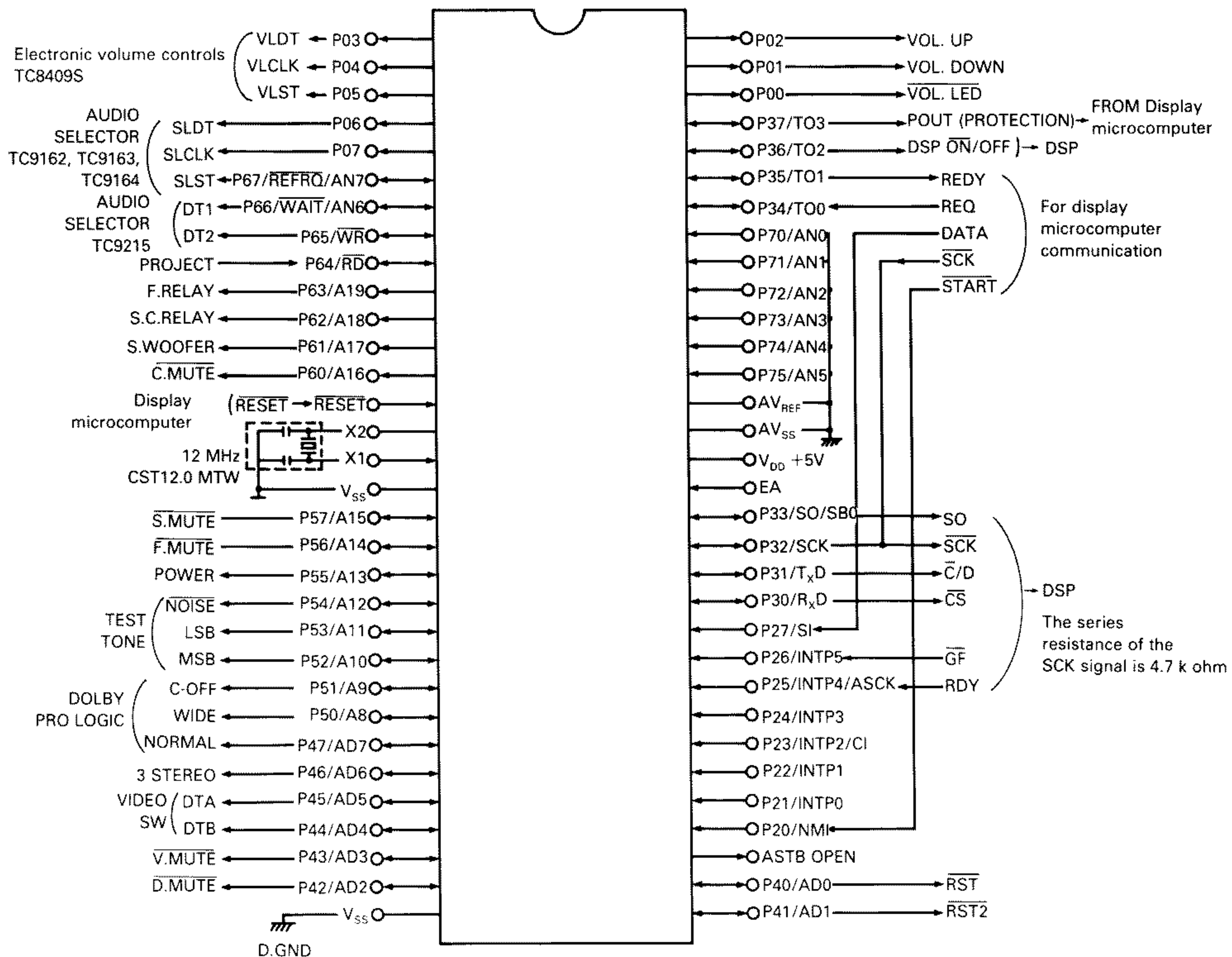
The slave microcomputer is used for DSP amplifiers. This microcomputer provides the following amplifier control functions through data communications with the display microcomputer (CXP50112-154Q):

- Selector selection (muting control) TC9162N, etc.
- Electronic volume control TC9213P

- Speaker relay control
- Surround control (DSP IC μ PD6380)
 - Dolby surround, Dolby Prologic, and DSP
 - Rear and Center levels (TC9213P)
 - Delay time (DSP IC μ PD6380)
 - Presence level (DSP IC μ PD6380)
- Video selection

2.3 Pin Connection (Top View)

64-pin plastic shrink DIP and 64-pin plastic QUIP



Note: Pin compatible with μ PD78210CW/GQ.

CIRCUIT DESCRIPTION

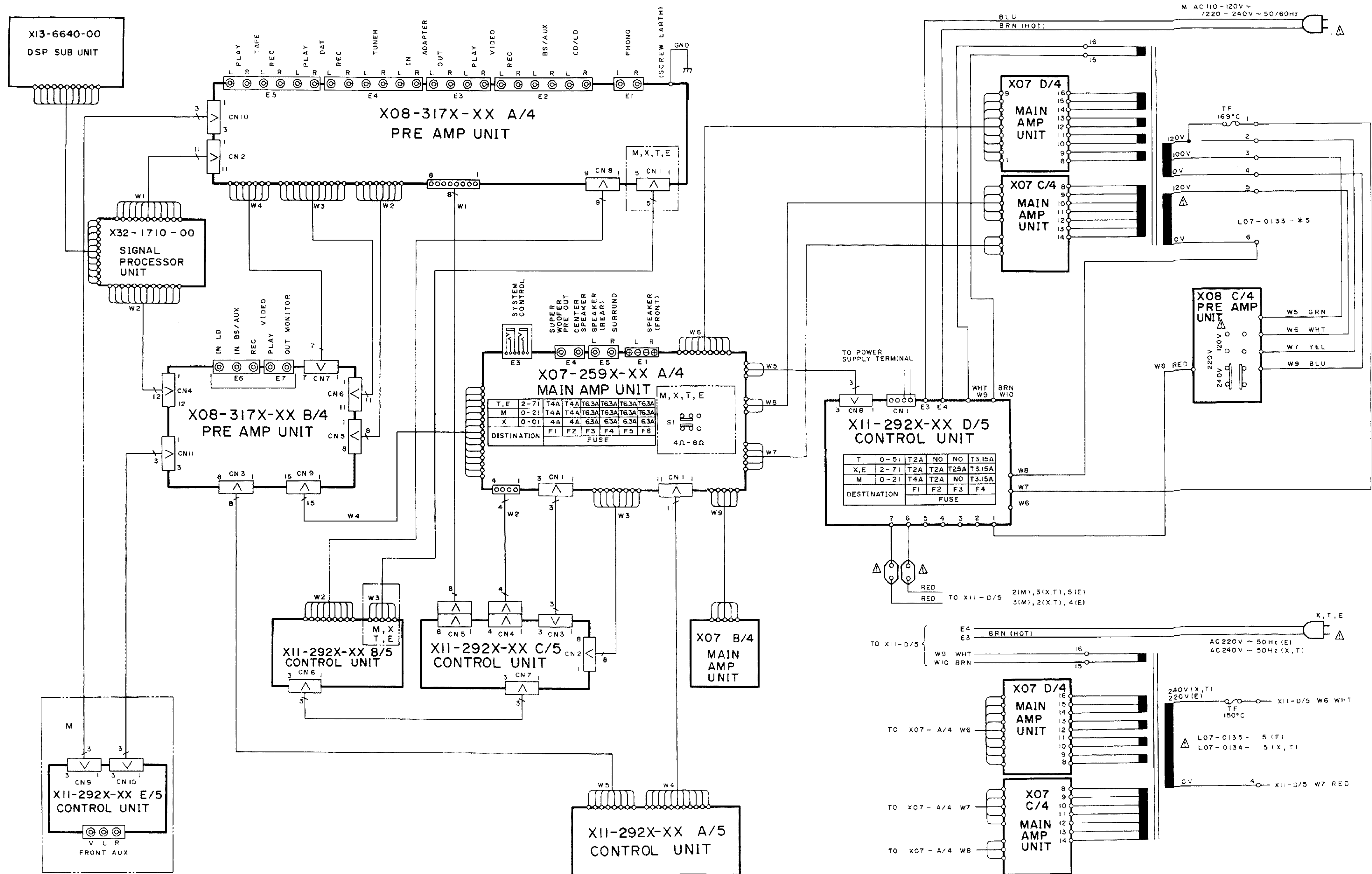
CIRCUIT DESCRIPTION

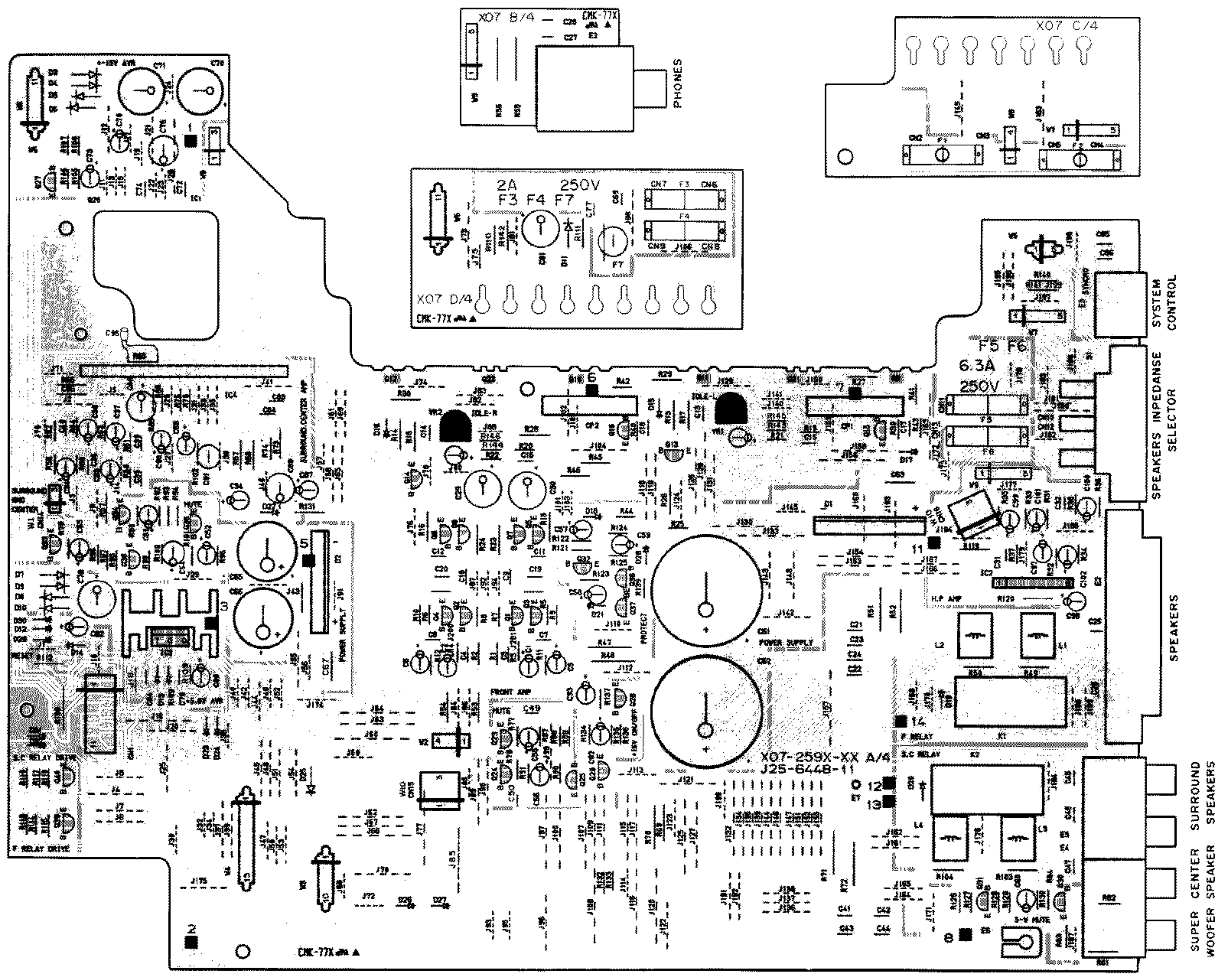
2.4 Pin Functions

Pin No.	Pin name	I/O	Name	Description																									
1	P03	O	VLDT	Electronic volume control IC (TC9213P) data output REAR =																									
2	P04	O	VLCLK	Electronic volume control IC (TC9213P) clock output LEFT																									
3	P05	O	VLST	Electronic volume control IC (TC9213P) strobe output CENTER =																									
4	P06	O	SLDT	Switch array IC (TC9162N, TC9163N, and TC9164N) data output RIGET																									
5	P07	O	SLCLK	Switch array IC (TC9162N, TC9163N, and TC9164N) clock output																									
6	P67	O	SLST	Switch array IC (TC9162N, TC9163N, and TC9164N) strobe output																									
7	P66	O	DT1	TC9215 DT1 signal output																									
				<table border="1"> <tr> <td></td> <td colspan="2">PROLOGIC</td> <td colspan="2">DOL SUR</td> </tr> <tr> <td>MODE</td> <td>DT1</td> <td>DT2</td> <td>DT1</td> <td>DT2</td> </tr> <tr> <td>OFF</td> <td>0</td> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>DOLBY</td> <td>0</td> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>DSP</td> <td>1</td> <td>0</td> <td>1</td> <td>0</td> </tr> </table>		PROLOGIC		DOL SUR		MODE	DT1	DT2	DT1	DT2	OFF	0	1	0	1	DOLBY	0	0	0	1	DSP	1	0	1	0
	PROLOGIC		DOL SUR																										
MODE	DT1	DT2	DT1	DT2																									
OFF	0	1	0	1																									
DOLBY	0	0	0	1																									
DSP	1	0	1	0																									
8	P65	O	DT2	TC9215 DT2 signal output																									
9	P64	I	PROTECT	Protection signal input Low: Normal High: Protected																									
10	P63	O	F.RELAY	Front speaker relay control Low: Speaker off High: Speaker on																									
11	P62	O	S.C.RELAY	Rear center speaker relay control Low: Speaker off High: Speaker on																									
12	P61	O	S.WOOFER	Super-woofer output control Low: No output High: Output																									
13	P60	O	CMUTE	Center output mute Low: Mute on High: Mute off																									
14	RESET	I		Reset High: Normal Low: Reset																									
15	X2			System clock oscillator connection (12 MHz)																									
16	X1	I		System clock oscillator connection (12 MHz)																									
17	V _{SS}			Ground																									
18	P57	O	S.MUTE	Rear output mute Low: Mute on High: Mute off																									
19	P56	O	F.MUTE	Front output mute Low: Mute on High: Mute off																									
20	P55	O	POWER	Power supply control pin Low: Power off High: Power on																									
21	P54	O	NOISE	Dolby Prologic test tone output control Low: Test tone off High: Test tone on																									
22	P53	O	LSB	Test tone output position specification																									
23	P52	O	MSB																										
				<table border="1"> <tr> <td></td> <td>L</td> <td>C</td> <td>R</td> <td>S</td> </tr> <tr> <td>LSB</td> <td>0</td> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>MSB</td> <td>0</td> <td>0</td> <td>1</td> <td>1</td> </tr> </table>		L	C	R	S	LSB	0	1	0	1	MSB	0	0	1	1										
	L	C	R	S																									
LSB	0	1	0	1																									
MSB	0	0	1	1																									
24	P51	O	C-OFF	Dolby Prologic center mode setting (center off)																									
25	P50	O	WIDE																										
26	P47	O	NORMAL																										
27	P46	O	3 STEREO	<table border="1"> <tr> <td></td> <td>NORMAL</td> <td>WIDE</td> <td>PHANTOM</td> </tr> <tr> <td>WIDE</td> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>NORMAL</td> <td>1</td> <td>0</td> <td>0</td> </tr> </table>		NORMAL	WIDE	PHANTOM	WIDE	0	1	0	NORMAL	1	0	0													
	NORMAL	WIDE	PHANTOM																										
WIDE	0	1	0																										
NORMAL	1	0	0																										
28	P45	O	DTA	Video selection control																									
29	P44	O	DTB																										
				<table border="1"> <tr> <td></td> <td>VIDEO</td> <td>AUX1</td> <td>CD/LD</td> <td>AUX2</td> </tr> <tr> <td>DTA T</td> <td>1</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>DTB</td> <td>1</td> <td>0</td> <td>1</td> <td>0</td> </tr> </table>		VIDEO	AUX1	CD/LD	AUX2	DTA T	1	1	0	0	DTB	1	0	1	0										
	VIDEO	AUX1	CD/LD	AUX2																									
DTA T	1	1	0	0																									
DTB	1	0	1	0																									
30	P43	O	V.MUTE	Video output muting																									

Pin No.	Pin name	I/O	Name	Description
31	P42	O	D.MUTE	DSP board output muting
32	V _{SS}			Ground
33	P41	O	RST2	DSP IC (μPD6380) RST2 signal output
34	P40	O	RST	DSP IC (μPD6380) RST signal output
35	ASTB	O		Unused (opened, pulled up, and pulled down)
36	P20/NMT	I		START signal input for communications with display microcomputer
37	P21	I		Unused (pulled up and pulled down)
				Unused (pulled up and pulled down)
40	P24	I		Unused (pulled up and pulled down)
41	P25	I	RDY	DSP IC (μPD6380) RDY signal input
42	P26	I	GF	DSP IC (μPD6380) GF signal input
43	P27/SI	I	DATA	Data signal input for communications with display microcomputer
44	P30	O	CS	DSP IC (μPD6380) CS signal output
45	P31	O	C/D	DSP IC (μPD6380) C/D signal output
46	P32	I/O	SCK	SCK signal input during communications with display microcomputer SCK signal output during DSP IC (μPD6380) data output
47	P33/SO	O	SO	DSP IC (μPD6380) SO (data) signal output
48	EA	I		Unused (ROMless operation instruction) Pulled up or pulled down.
49	V _{DD}			Microcomputer power supply
50	AV _{SS}			Unused (ground for analog-to-digital converter) Ground
51	AV _{REF}			Unused (Reference voltage for analog-to-digital converter is applied) Ground
52	P75/AN5	I		Unused Ground GND
				~ ~
57	P70/AN0	I		Unused Ground GND
58	P34/TO0	I	REQ	REQ signal input for communications with display microcomputer
59	P35/TO1	I	REDY	REDY signal output for communications with display microcomputer
60	P36/TO2	O	DSP ON/OFF	DSP oscillator stop control on DSP board High: Stop Low: Oscillate
61	P37/TO3	O	POUT	Protection status report signal to display microcomputer High: Protected Low: Normal
62	P00	O	VOL.LED	Electronic volume control LED drive Low: Lights High: Goes off
63	P01	O	VOL.DOWN	Electronic volume control down signal output
64	P02	O	VOL.UP	Electronic volume control up signal output

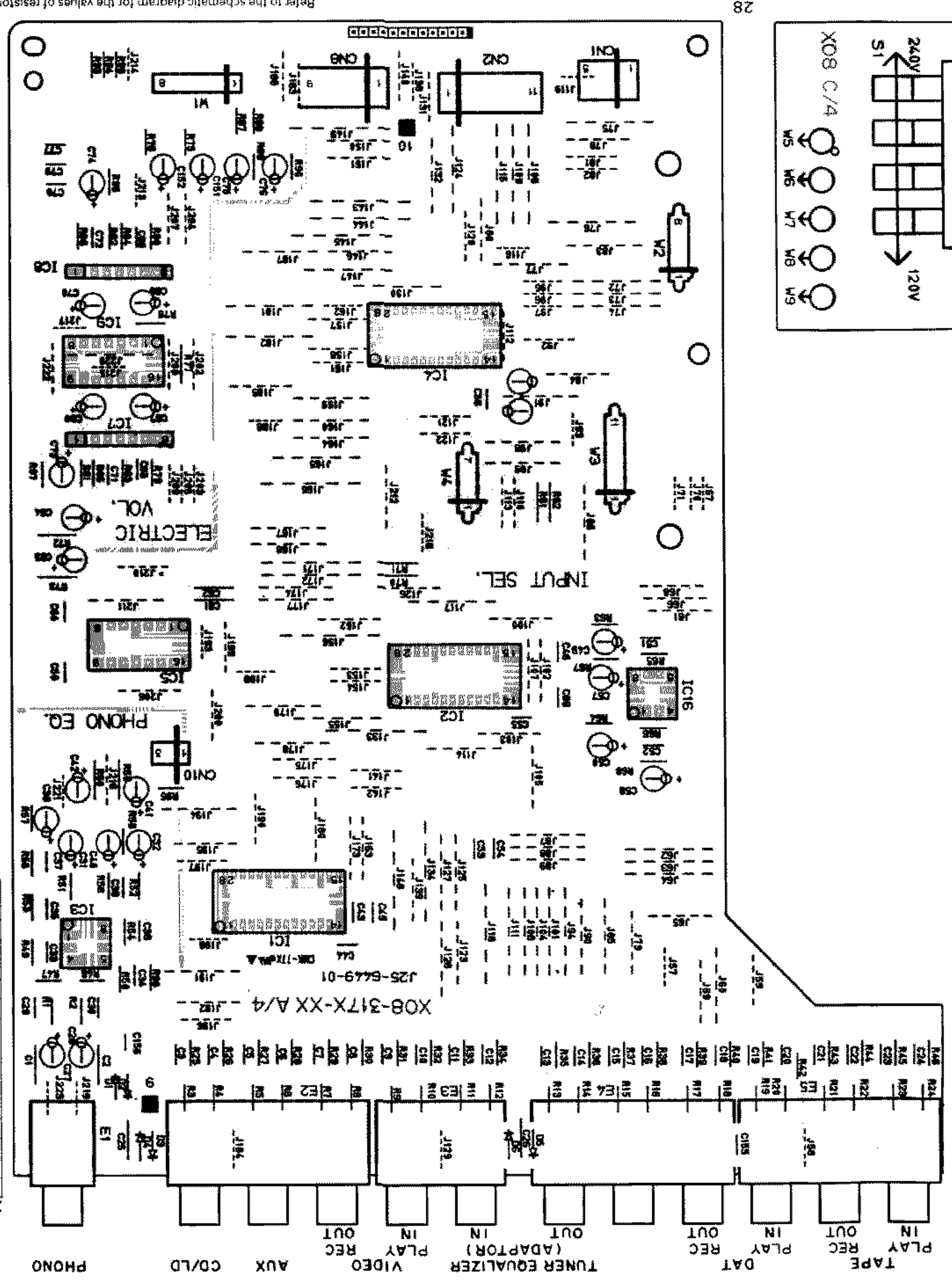
WIRING DIAGRAM





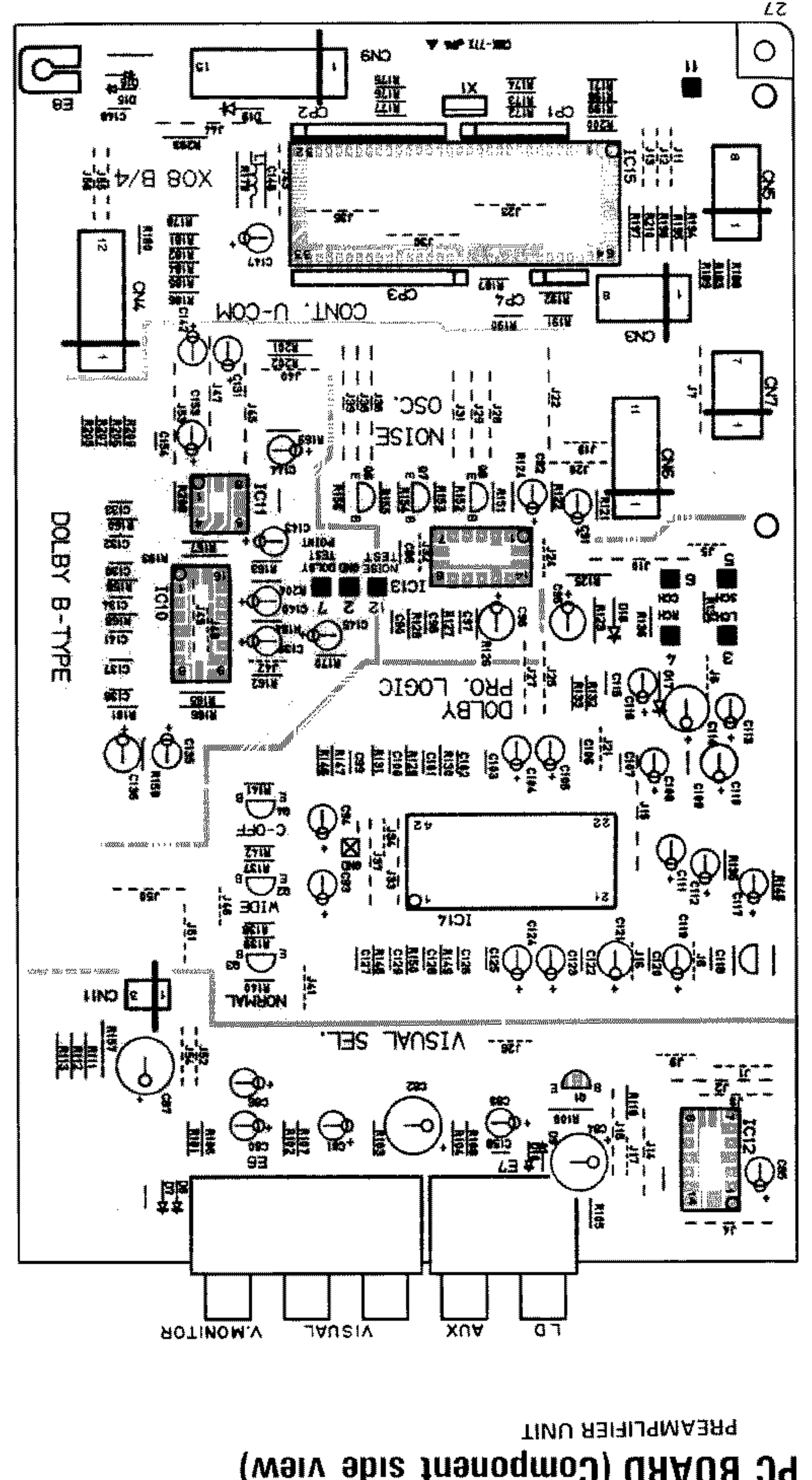
Ref. No.	Q	Address
1	5E	
2	5D	
3	5E	
4	5D	
5	4E	
6	4D	
7	4E	
8	4D	
9	4G	
10	4E	
11	4F	
12	4D	
13	4F	
14	4D	
15	4G	
16	4E	
21	4F	
22	4E	
23	6E	
24	6E	
25	6E	
26	2B	
27	2B	
28	5E	
29	6E	
30	6H	
31	6H	
32	4E	
33	4B	
34	4B	
35	4B	
36	4C	
37	5E	
38	5E	
39	6B	
40	6B	
1	2C	
2	5H	
3	5C	

Refer to the schematic diagram for the values of resistors and capacitors



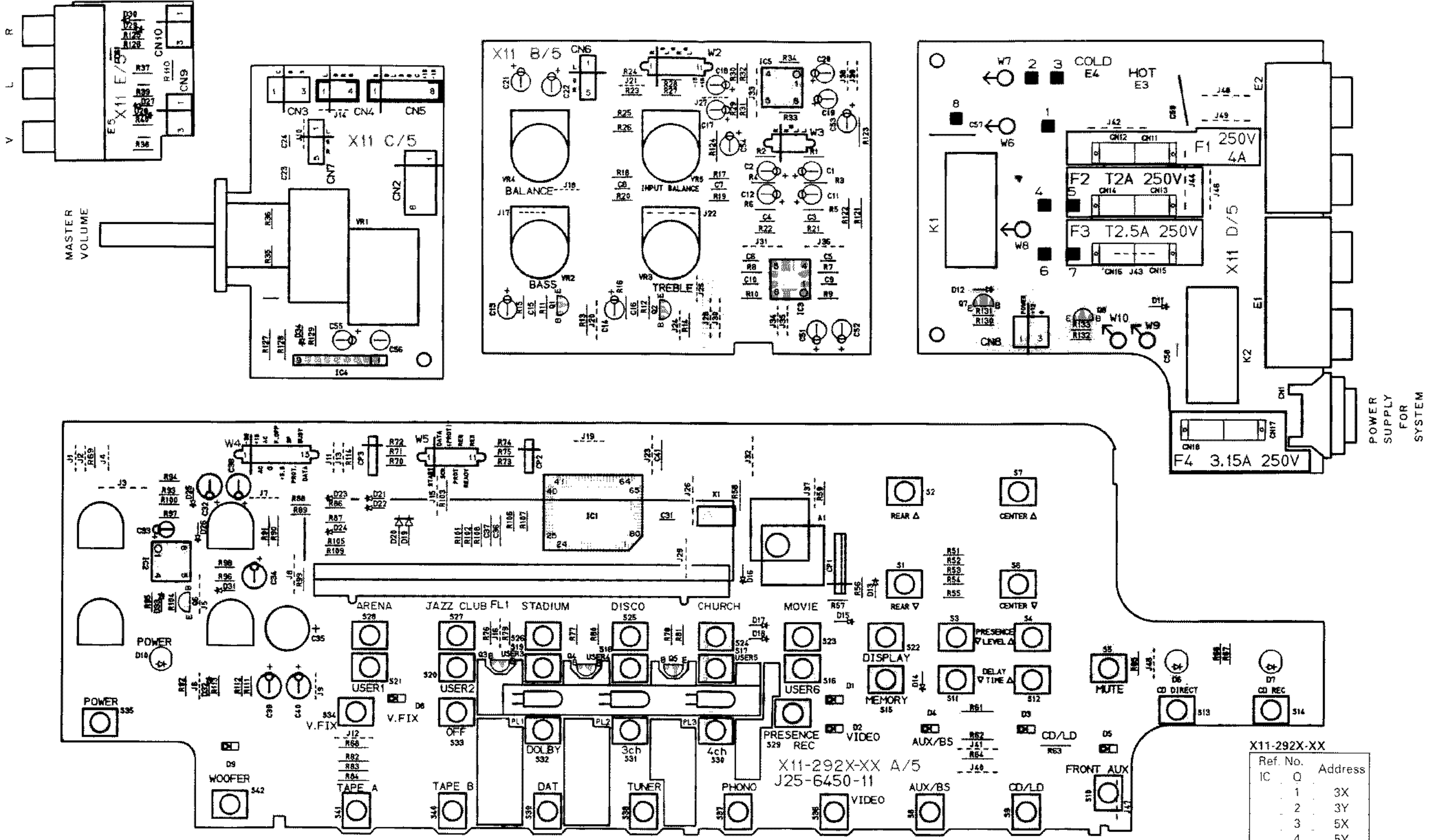
Ref. No.	Q	Address
16	4P	
15	6L	
12	2K	
11	5M	
10	4M	
9	6S	
8	6S	
7	5S	
5	4S	
4	5O	
3	3S	
2	4Q	
1	3R	
2L		
IC	0	
IC	1	

Refer to the schematic diagram for the values of resistors and capacitors



Refer to the schematic diagram for the values of resistors and capacitors

PC BOARD (Component side view) CONTROL UNIT



Refer to the schematic diagram for the values of resistors and capacitors.

X11-292X-XX		
Ref. No.	IC	Address
1	3X	
2	3Y	
3	5X	
4	5Y	
5	5Y	
6	5V	
7	3AA	
8	3AB	
1	4Y	
2	5V	
3	3Z	
4	3W	
5	1Z	

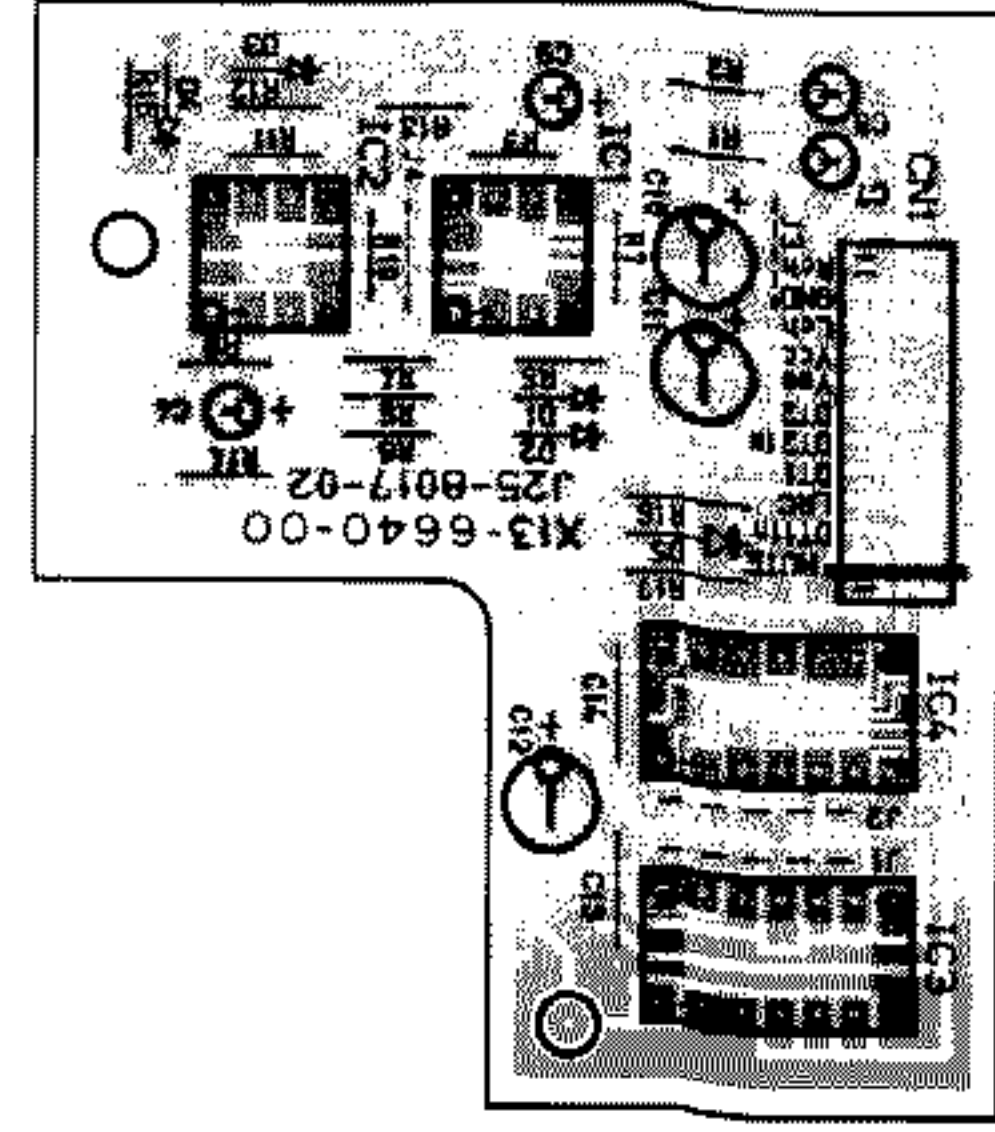
29

30

32

31

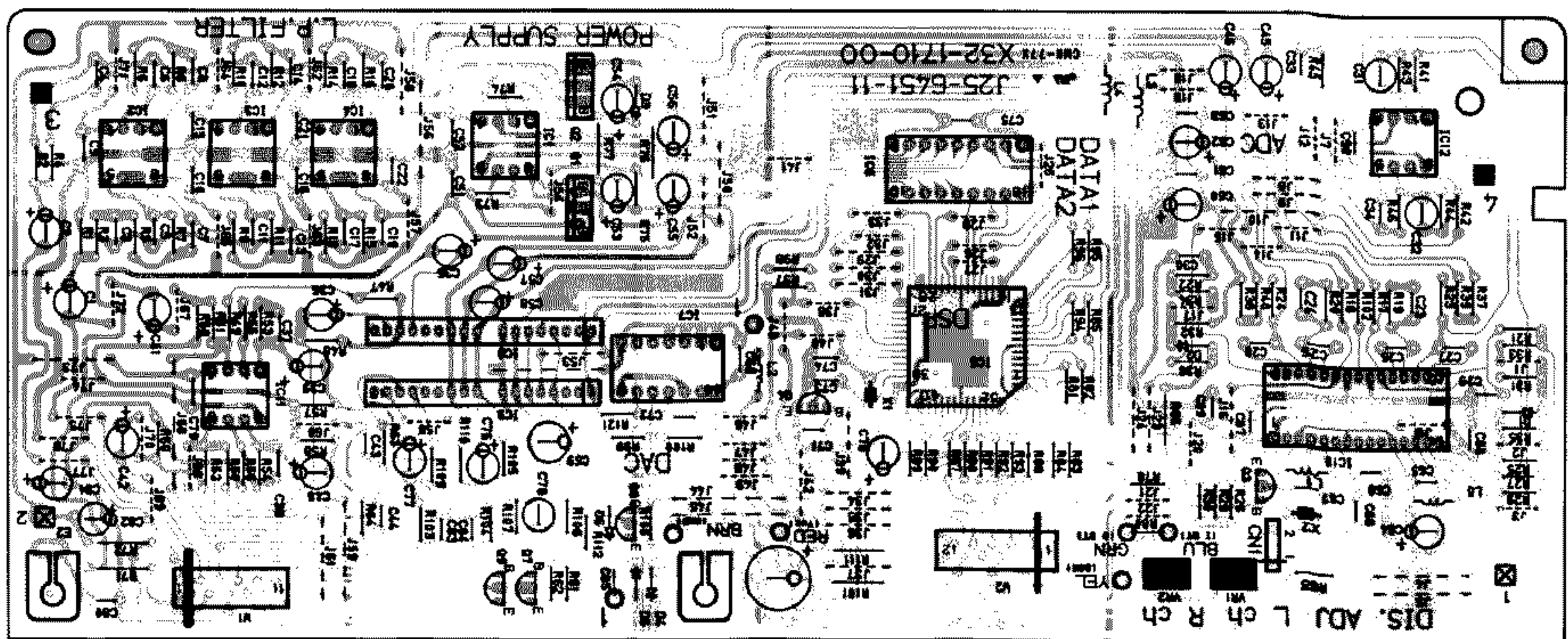
Refer to the schematic diagram for the values of resistors and capacitors.



DSP SUB UNIT

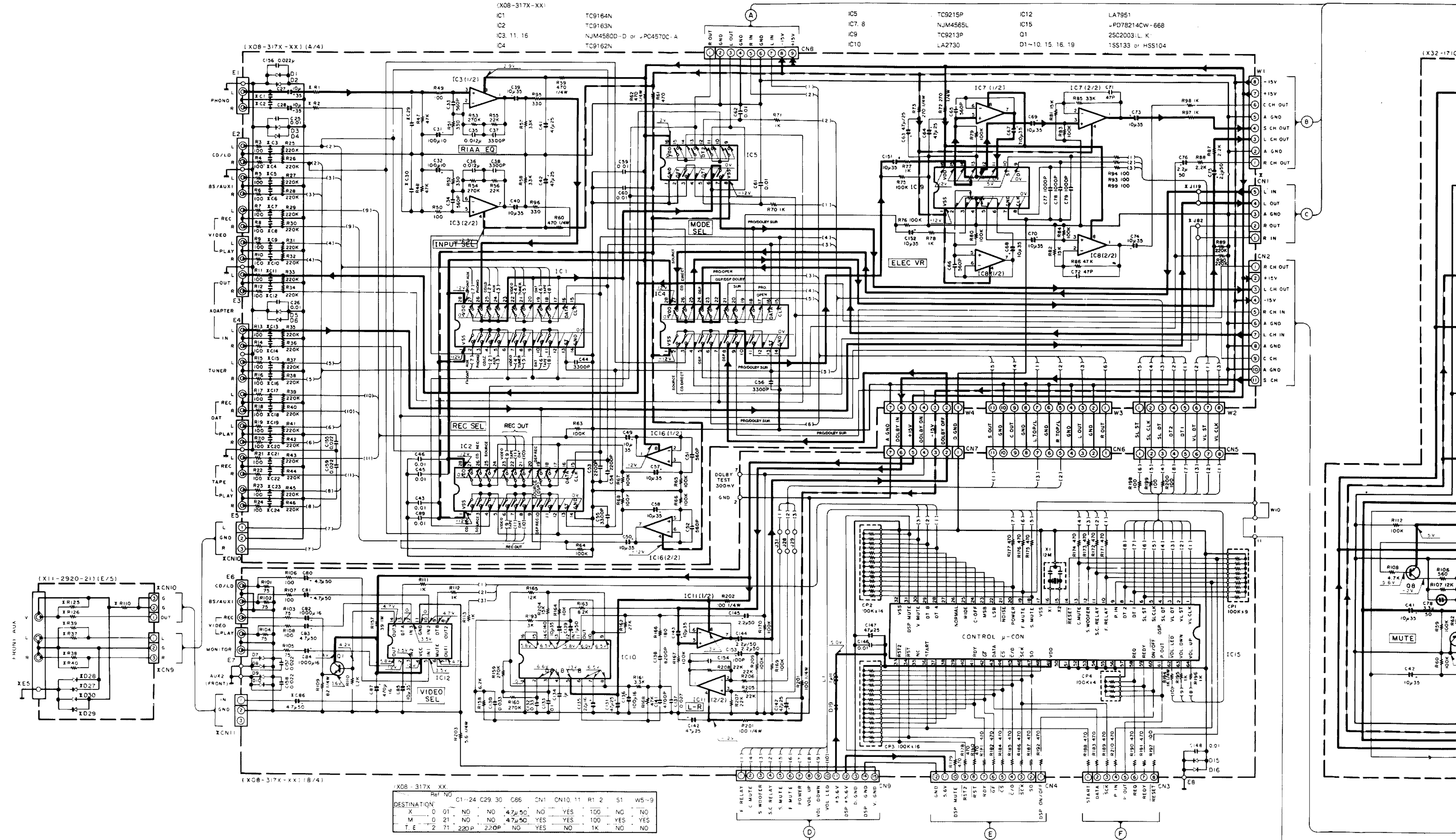
X32-1710-00		
Ref. No.	IC	Address
1	3A1	
2	3A1	
3	2AG	
4	2A1	
7	2A1	
8	2A1	
9	1AJ	
1	3AJ	
2	2AK	
3	2AK	
4	3AJ	
5	2AH	
6	3AH	
7	2AI	
8	2AJ	
9	2AJ	
10	2AF	
11	2AK	
12	3AF	

X13-6640-XX		
Ref. No.	IC	Address
1	6AG	
2	6AG	
3	5AF	
4	5AF	



SIGNAL PROCESSOR UNIT

PC BOARD (Component side view)

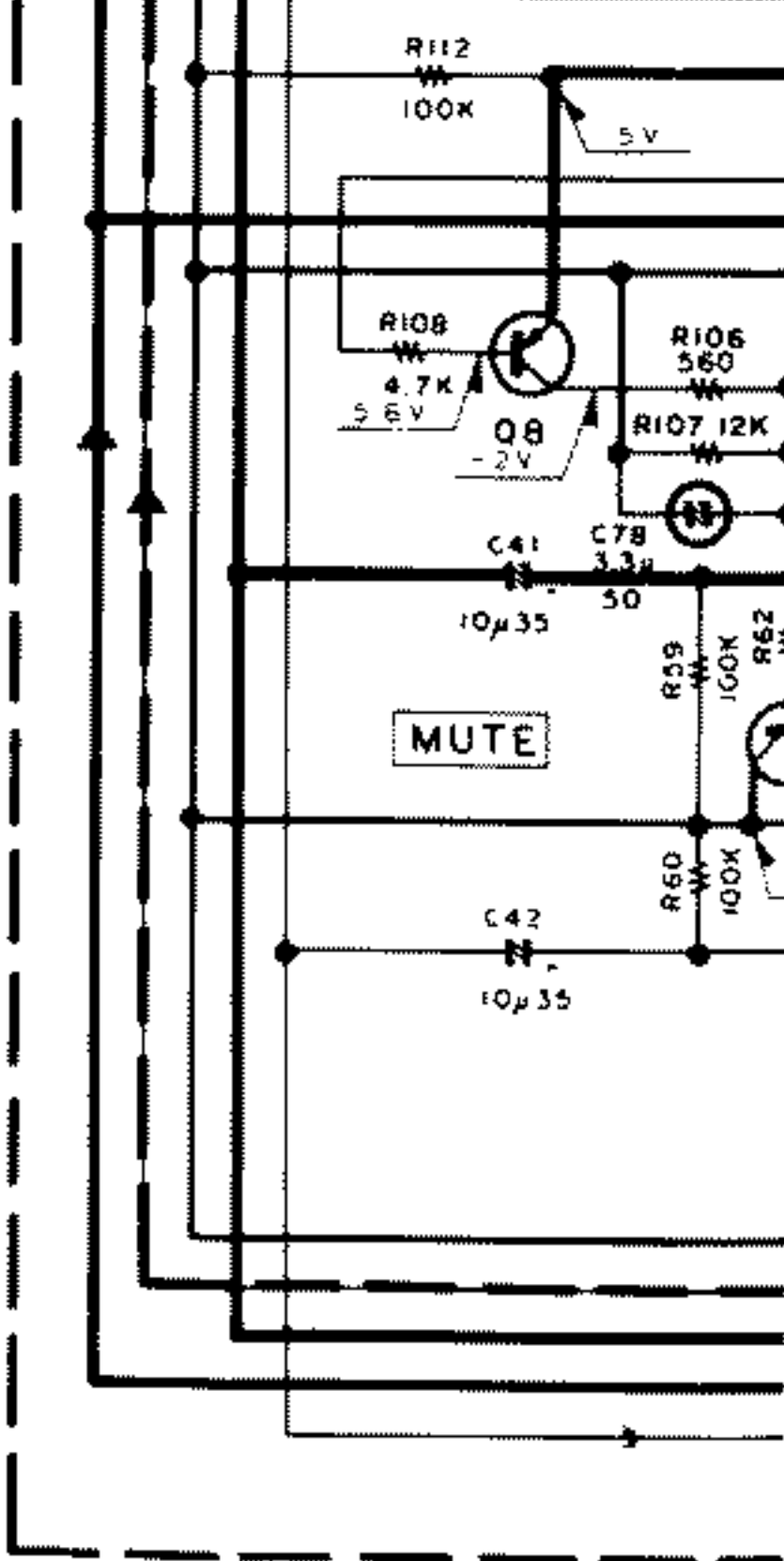
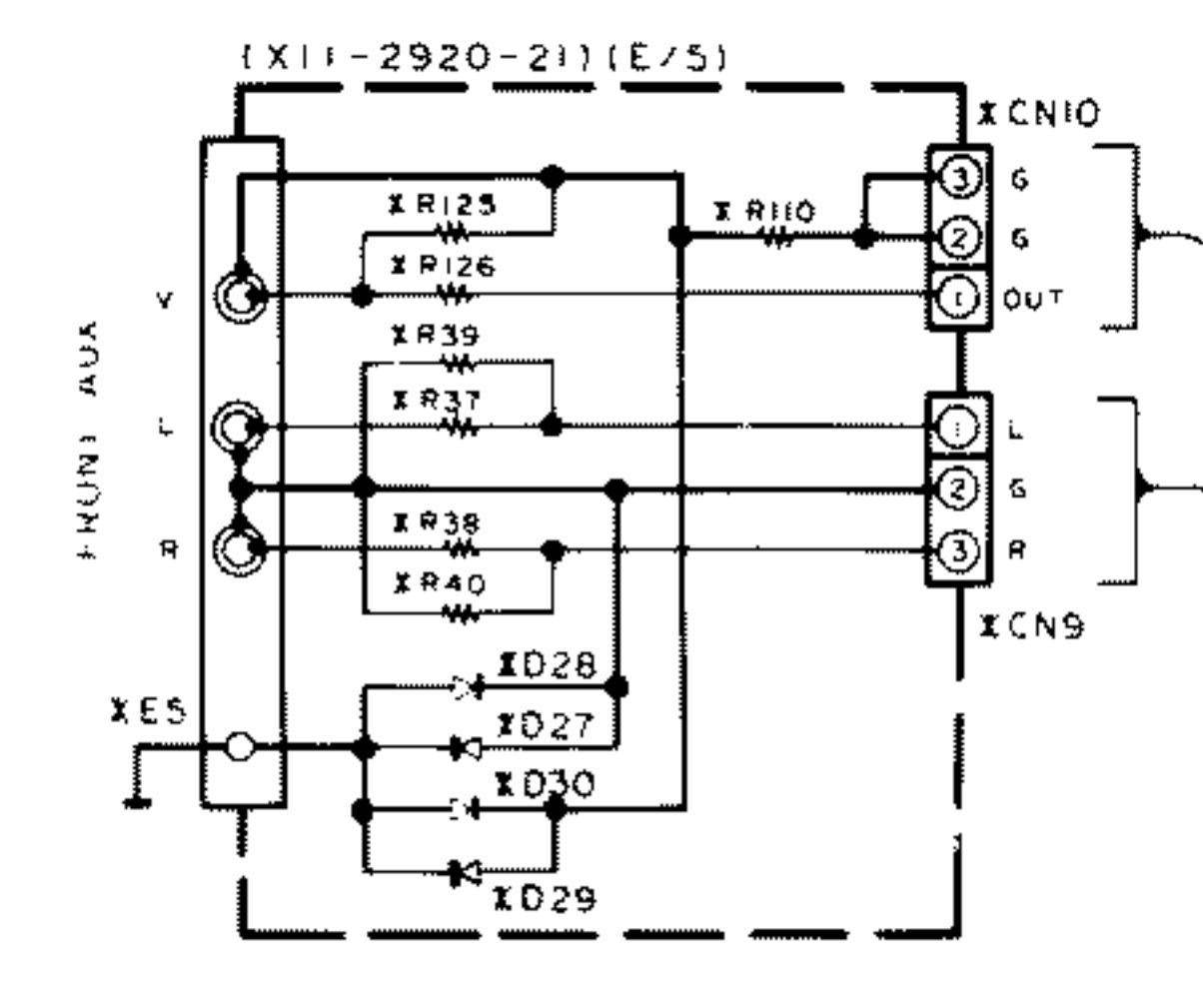


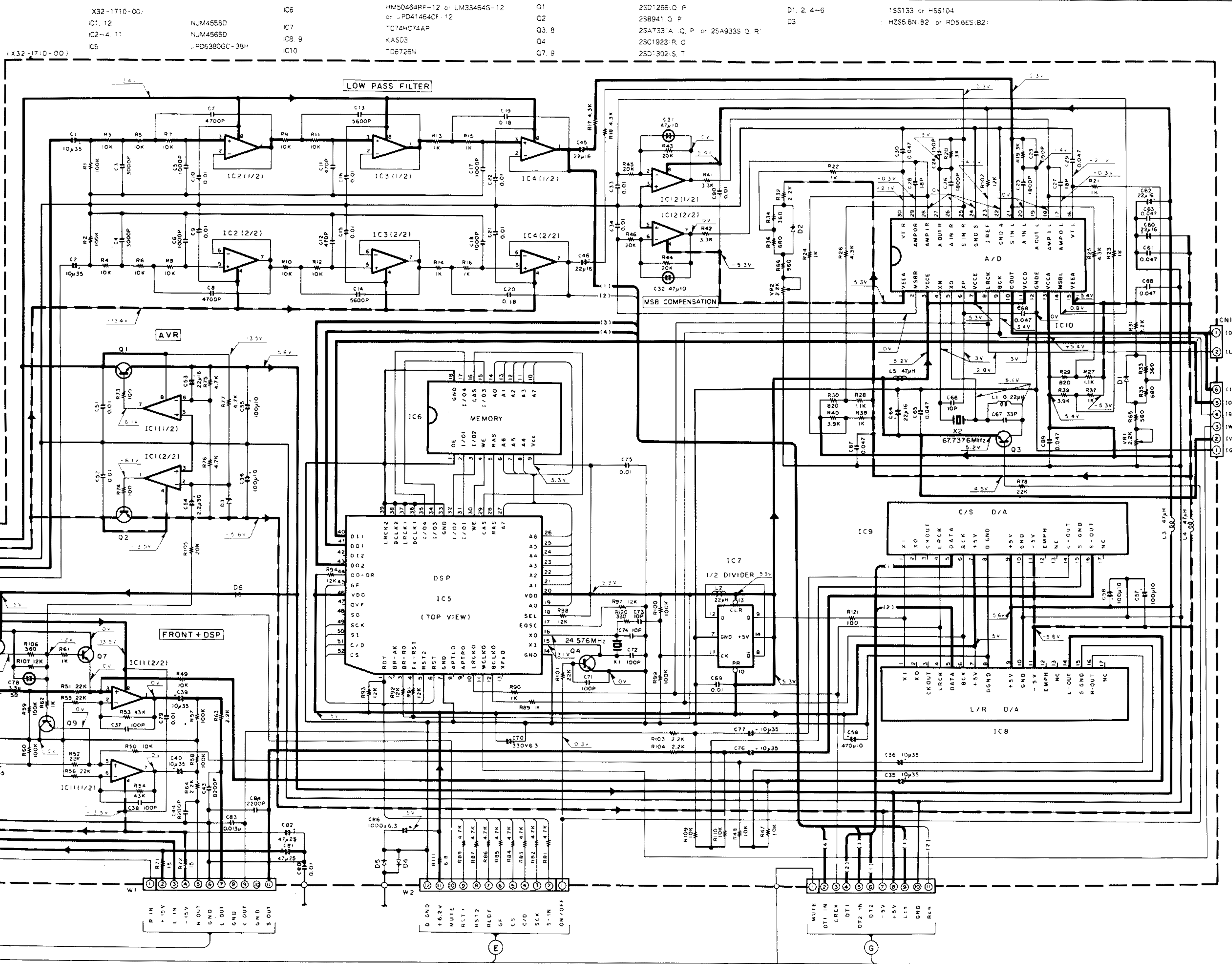
- (X08-317X-XX) (A/4)
- IC1 TC9164N
 - IC2 TC9163N
 - IC3 11, 16 NJM4560D-D or JPC4570C-A
 - IC4 TC9162N
 - IC5 LA7951
 - IC7, 8 NJM4565L
 - IC9 TC9213P
 - IC10 LA2730
 - IC12 PD78214CW-668
 - IC15 Q1 25C2003; L, K
 - D1-10, 15, 16, 19 1SS133 or HSS104

(X08-317X-XX) (B/4)

Ref. NO

DESTINATION	C1-24	C29, 30	C86	CN1	CN10, 11	R1, 2	S1	WS-3
X	0	01	NO	47 μ 50	NO	YES	100	NO
M	0	21	NO	NO	47 μ 50	YES	YES	100
T, E	2	71	220P	220P	NO	YES	NO	1K





- | | | |
|-----------|--|----------|
| 2SA1123 | | 2SA933S |
| 2SA733(A) | | 2SC1740S |
| 2SA992 | | |
| 2SC1845 | | |
| 2SC1923 | | |
| 2SC2003 | | |
| 2SC2878 | | |
| 2SC945(A) | | |
| 2SD1302 | | |
-
- | | | |
|------------|--|--|
| 2SD1266 | | |
| 2SC4137 | | |
| 2SB941 | | |
| NJM4558D | | |
| LA7951 | | |
| TC74HC74AP | | |
| LA2730 | | |
| TC9213P | | |
| TC9215P | | |
| TA8409S | | |
| BA10393 | | |
| NJM4565D | | |
| UPC4570C-A | | |
| UPC7805HF | | |
| UPC7815HF | | |
| TC9162N | | |
| TC9163N | | |
| TC9164N | | |
| TD6726N | | |

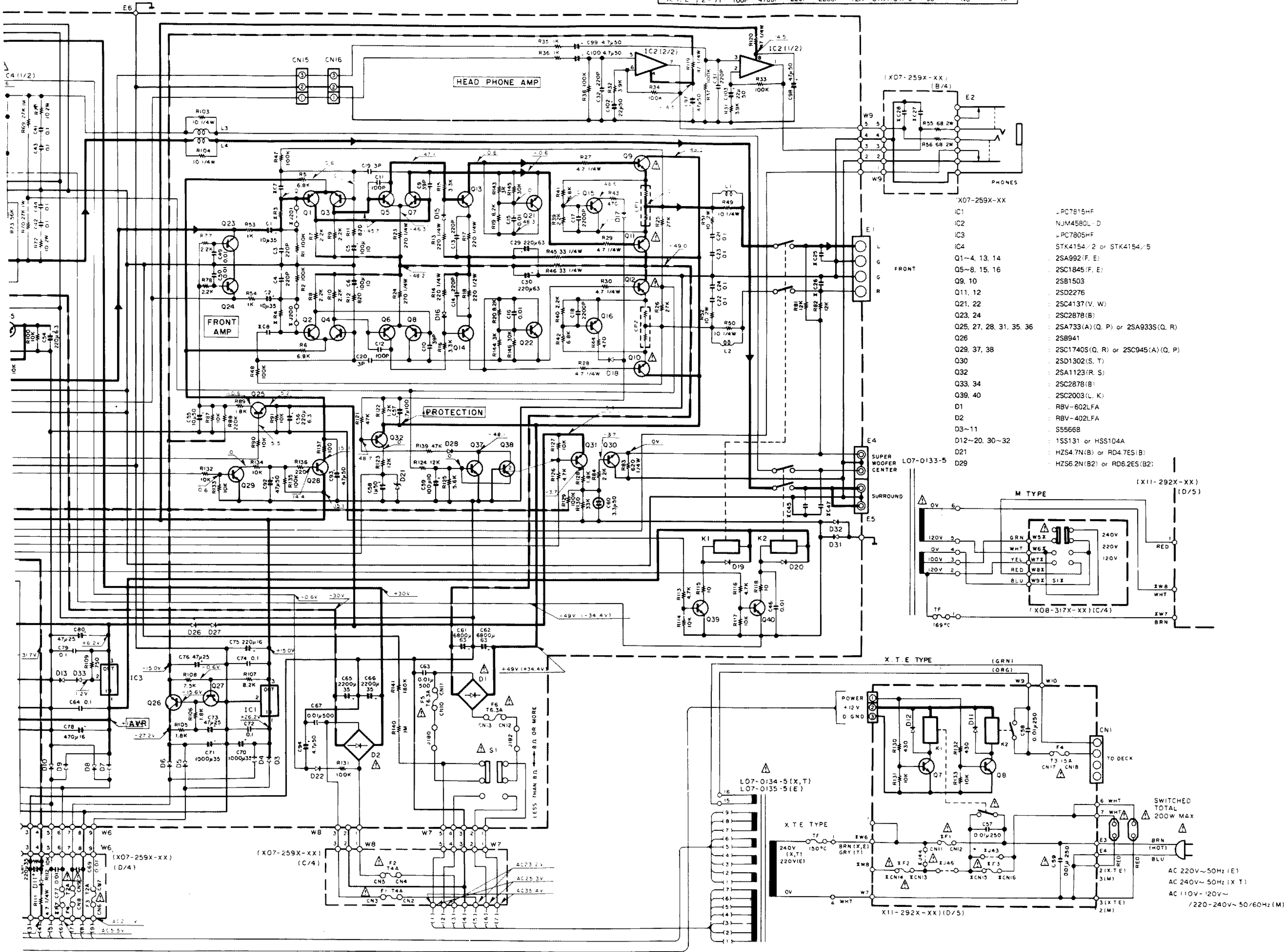
CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

▲ Others
DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

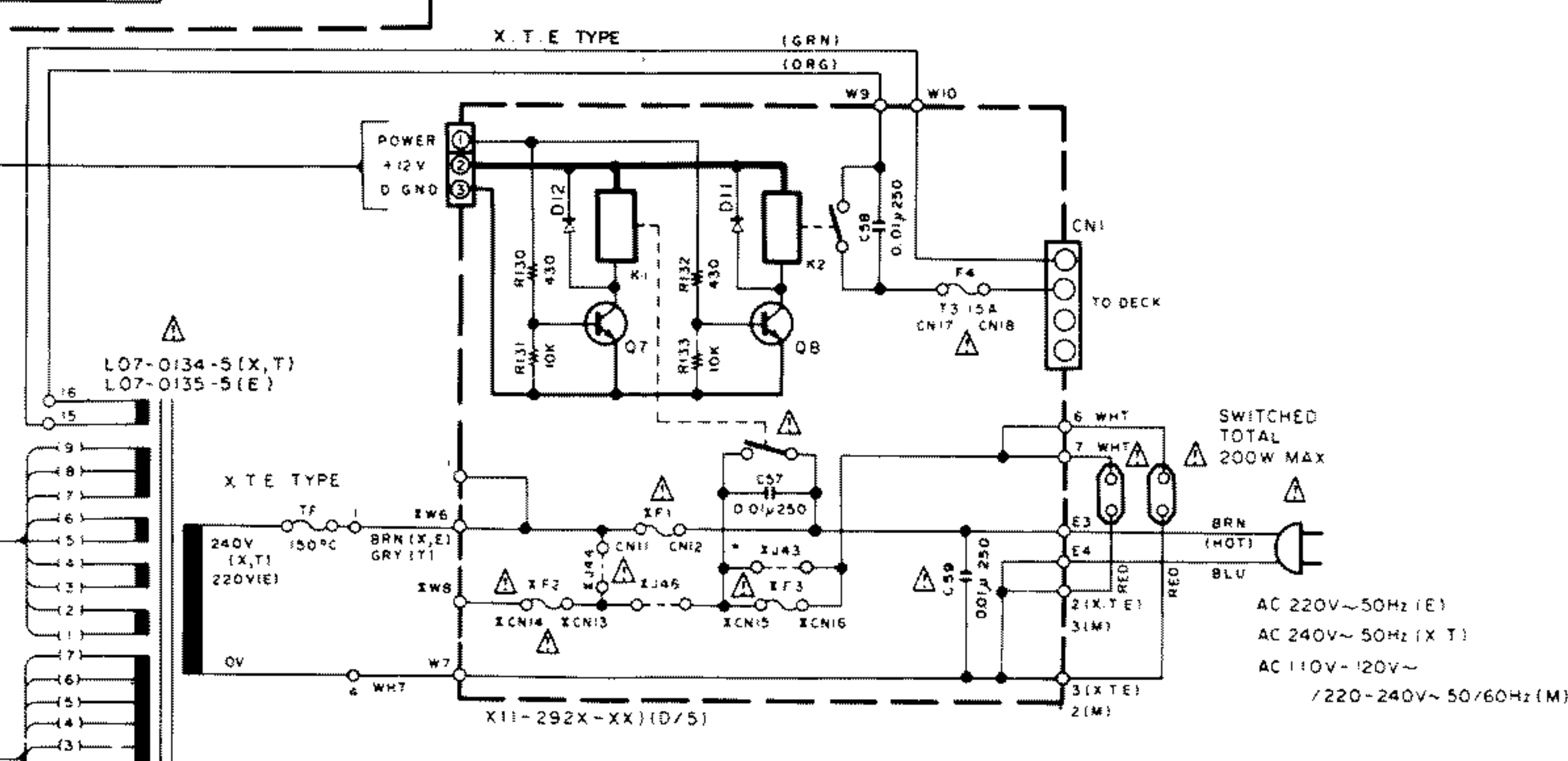
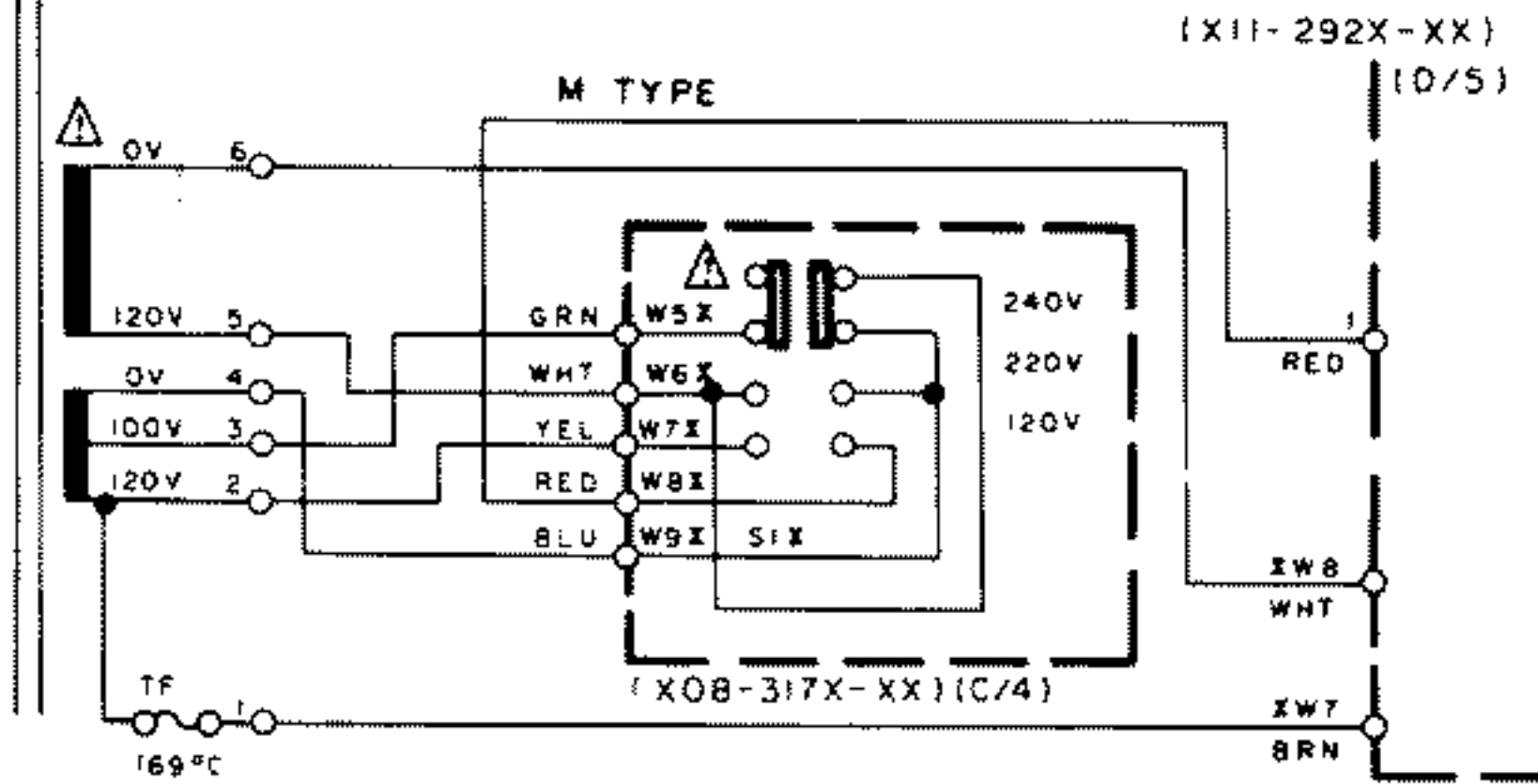
Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u. U. geringfügig.

(X07 - 259X - XX)		Ref. NO	
C7, 8	C25, 26	C27, 28	C45, 47
F7	IC4	J71	J79, 200, 201
R3, 4			
DESTINATION		NO	
M	0-21	NO	NO
X, T, E	2-71	100P	4700P
		220P	2200P
		T2A	STK4154/5
		YES	NO
		NO	1k



- (X07-259X-XX)
- IC1 PC7815HF
 - IC2 NJM4580L D
 - IC3 PC7805HF
 - IC4 STK4154/2 or STK4154/5
 - Q1-4, 13, 14 2SA992(F, E)
 - Q5-8, 15, 16 2SC1845(F, E)
 - Q9, 10 2SB1503
 - Q11, 12 2SD2276
 - Q21, 22 2SC4137(V, W)
 - Q23, 24 2SC2878(B)
 - Q25, 27, 28, 31, 35, 36 2SA733(A)(Q, P) or 2SA933S(Q, R)
 - Q26 2SB941
 - Q29, 37, 38 2SC1740S(Q, R) or 2SC945(A)(Q, P)
 - Q30 2SD1302(S, T)
 - Q32 2SA1123(R, S)
 - Q33, 34 2SC2878(B)
 - Q39, 40 2SC2003(L, K)
 - D1 RBV-602LFA
 - D2 RBV-402LFA
 - D3-11 S5566B
 - D12-20, 30-32 1SS131 or H5S104A
 - D21 HZS47N(B) or RD47ES(B)
 - D29 HZS62N(B2) or RD62ES(B2)



CAUTION: For continued safety, replace safety critical components only with manufacturer's recommended parts (refer to parts list). **⚠** Indicates safety critical components. To reduce the risk of electric shock, leakage-current or resistance measurements shall be carried out (exposed parts are acceptably insulated from the supply circuit) before the appliance is returned to the customer.

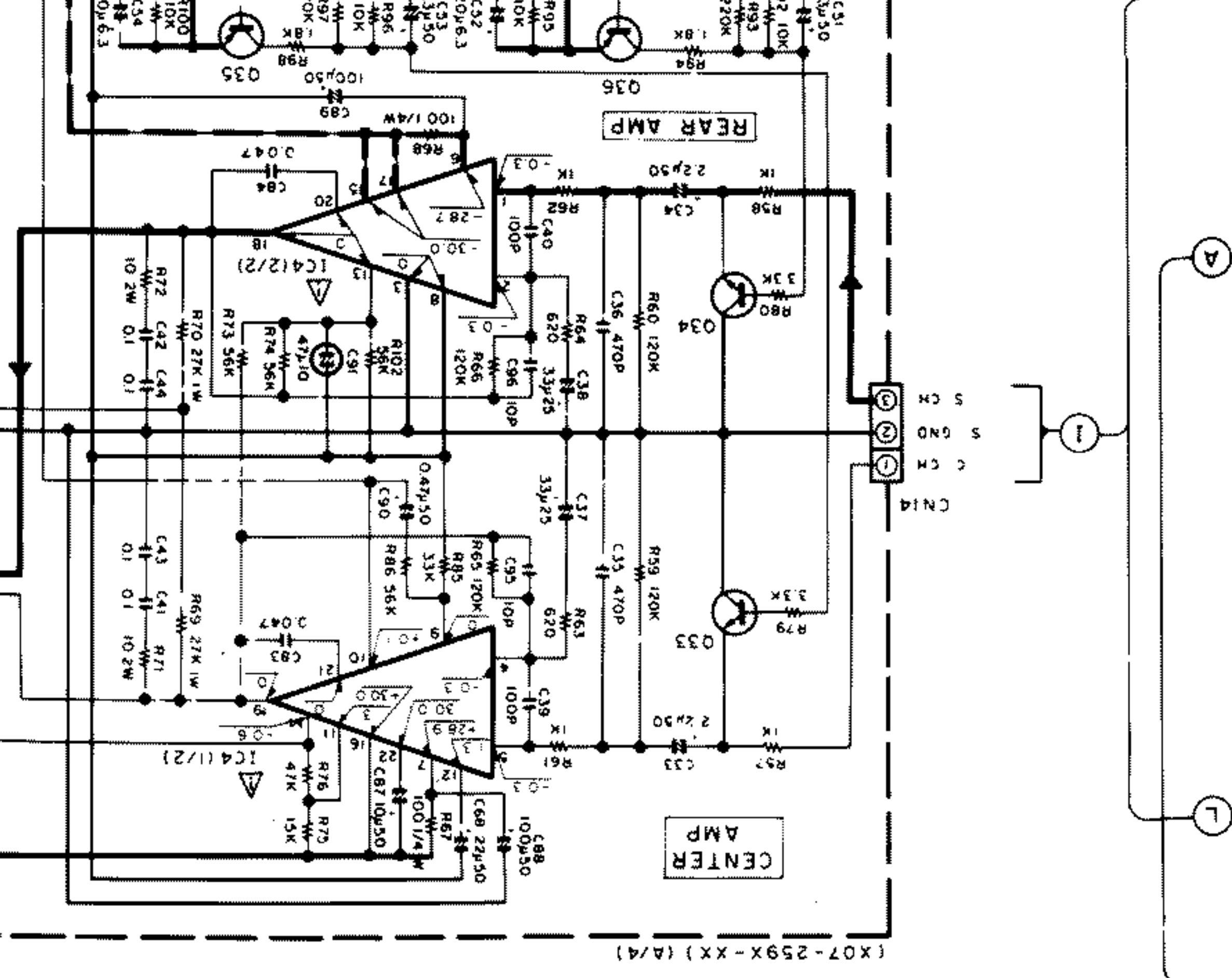
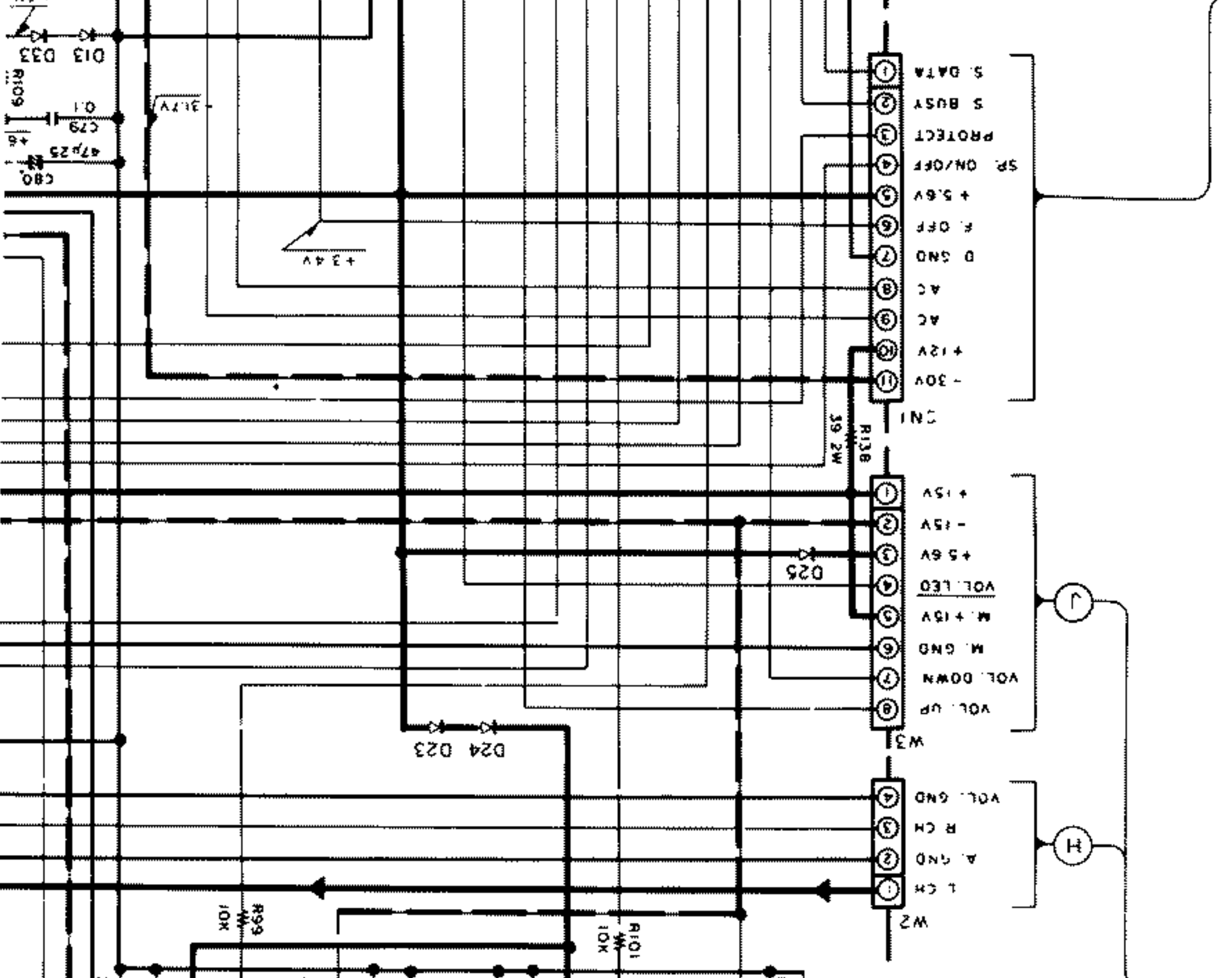
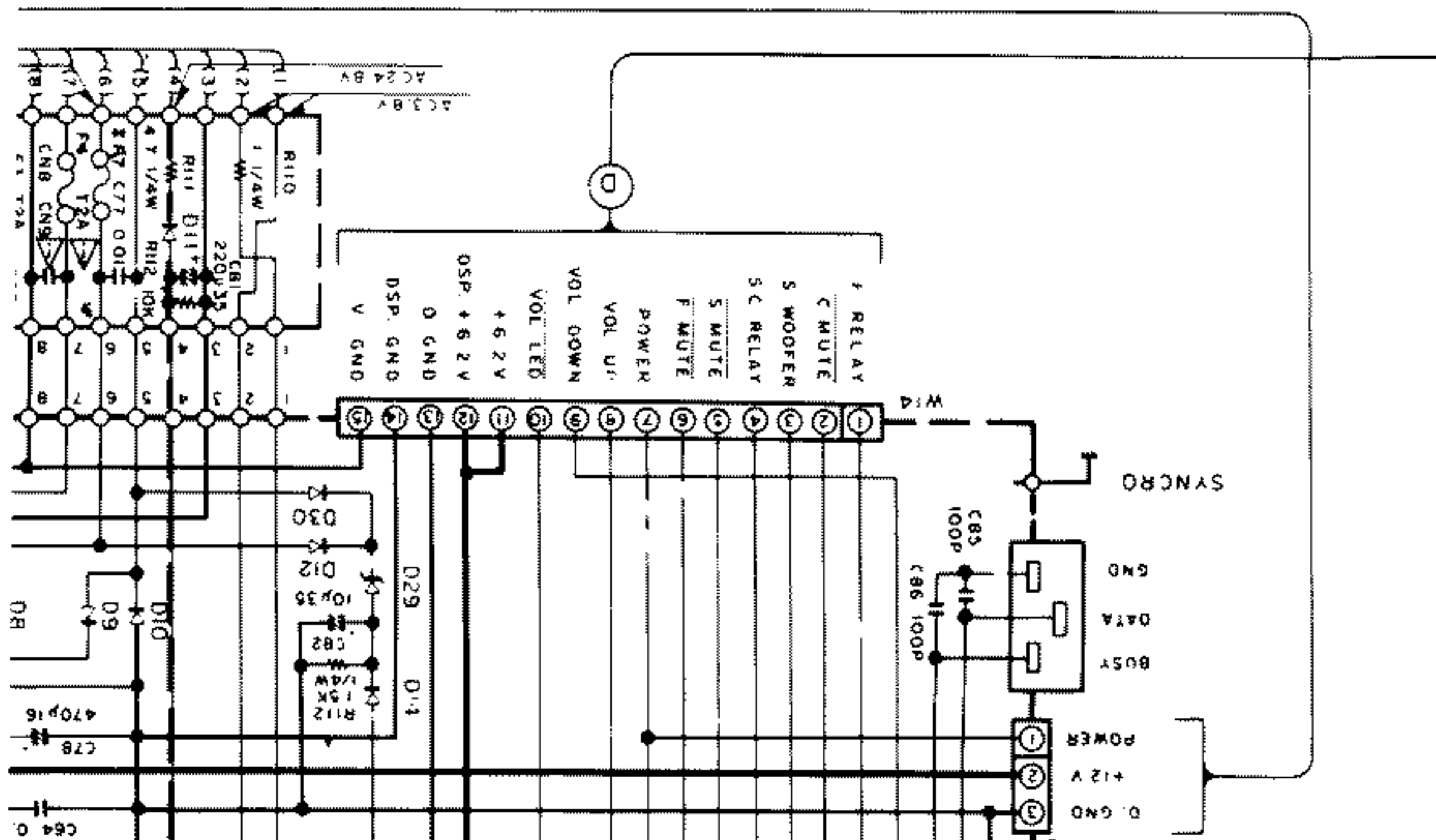
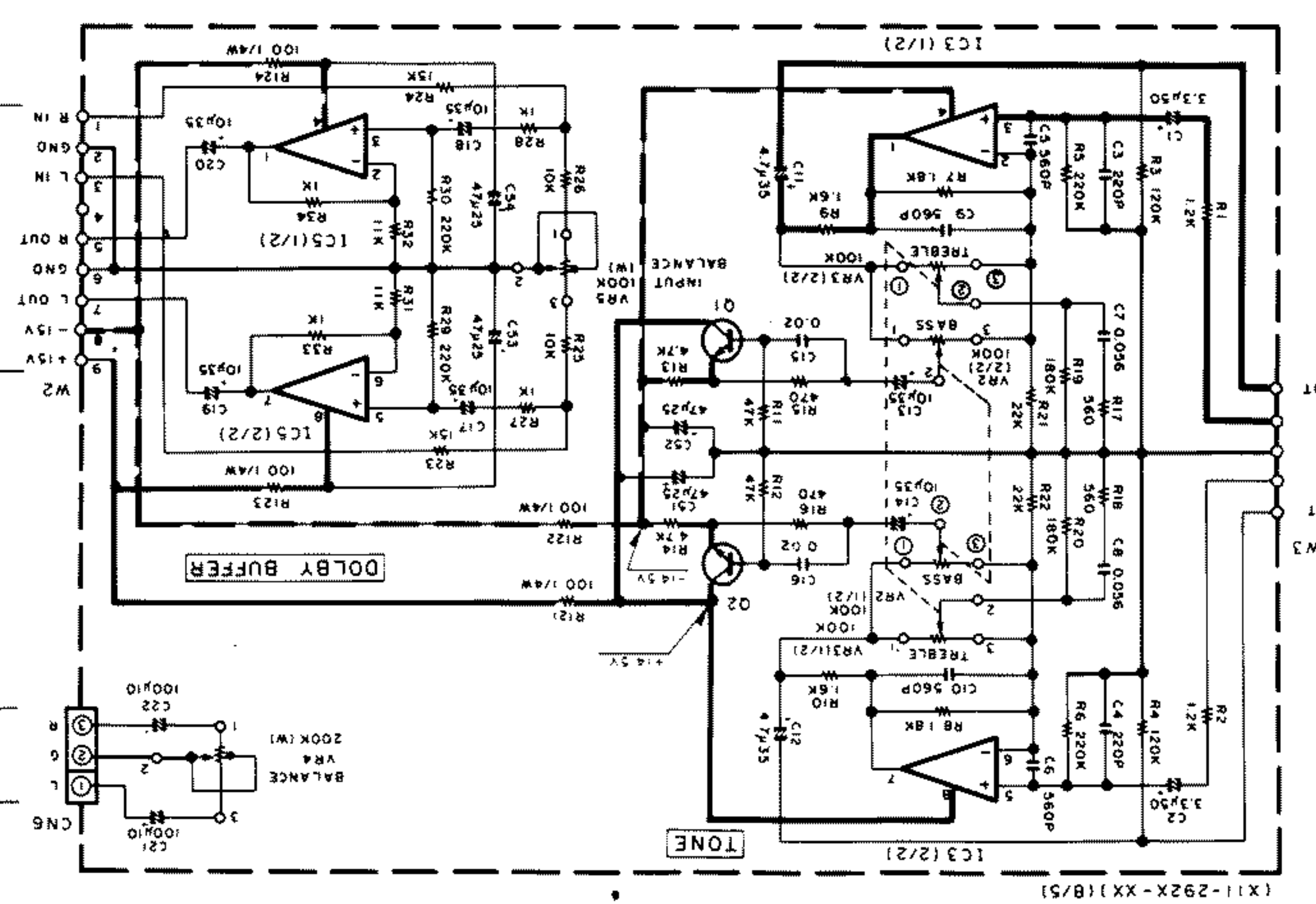
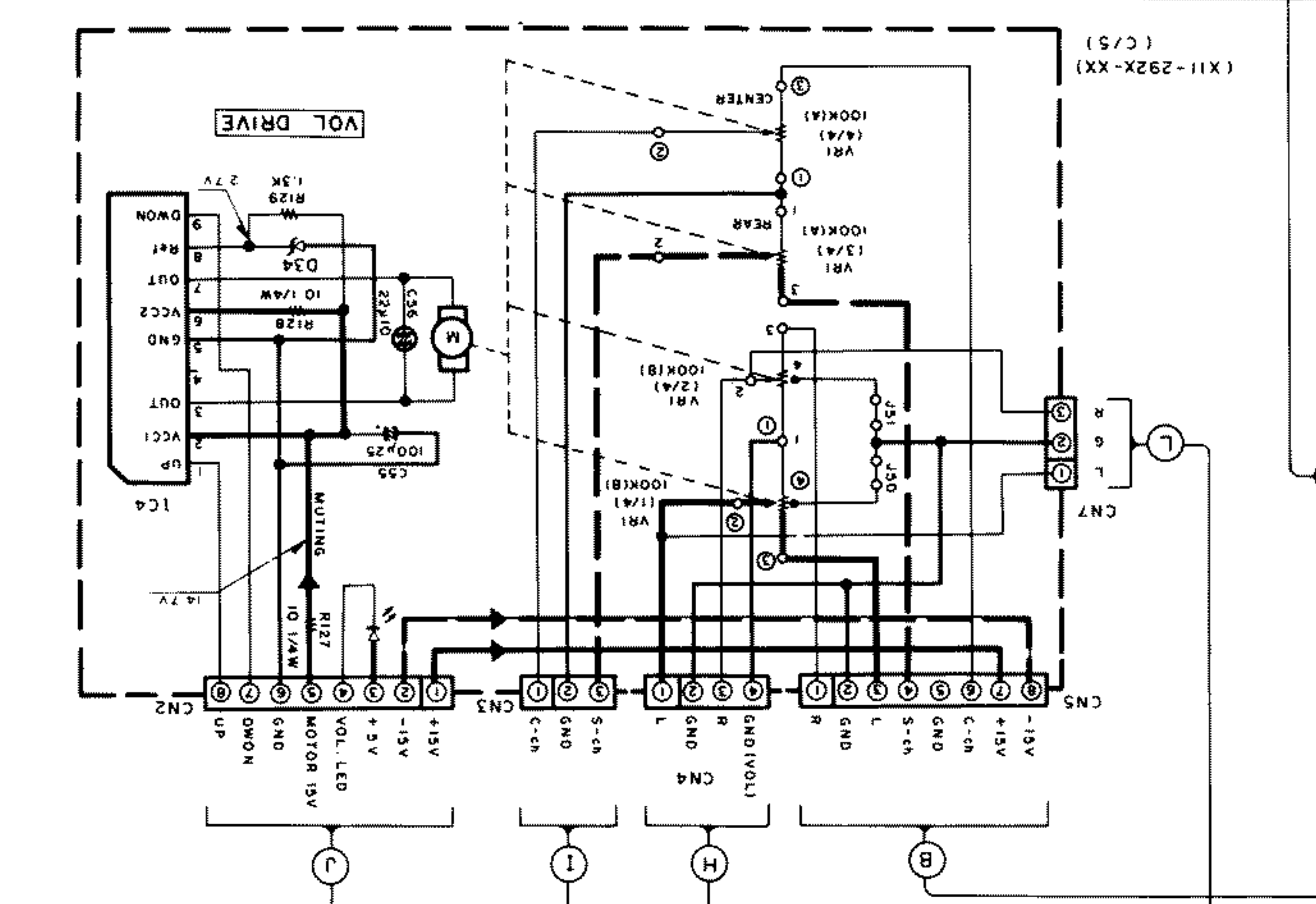
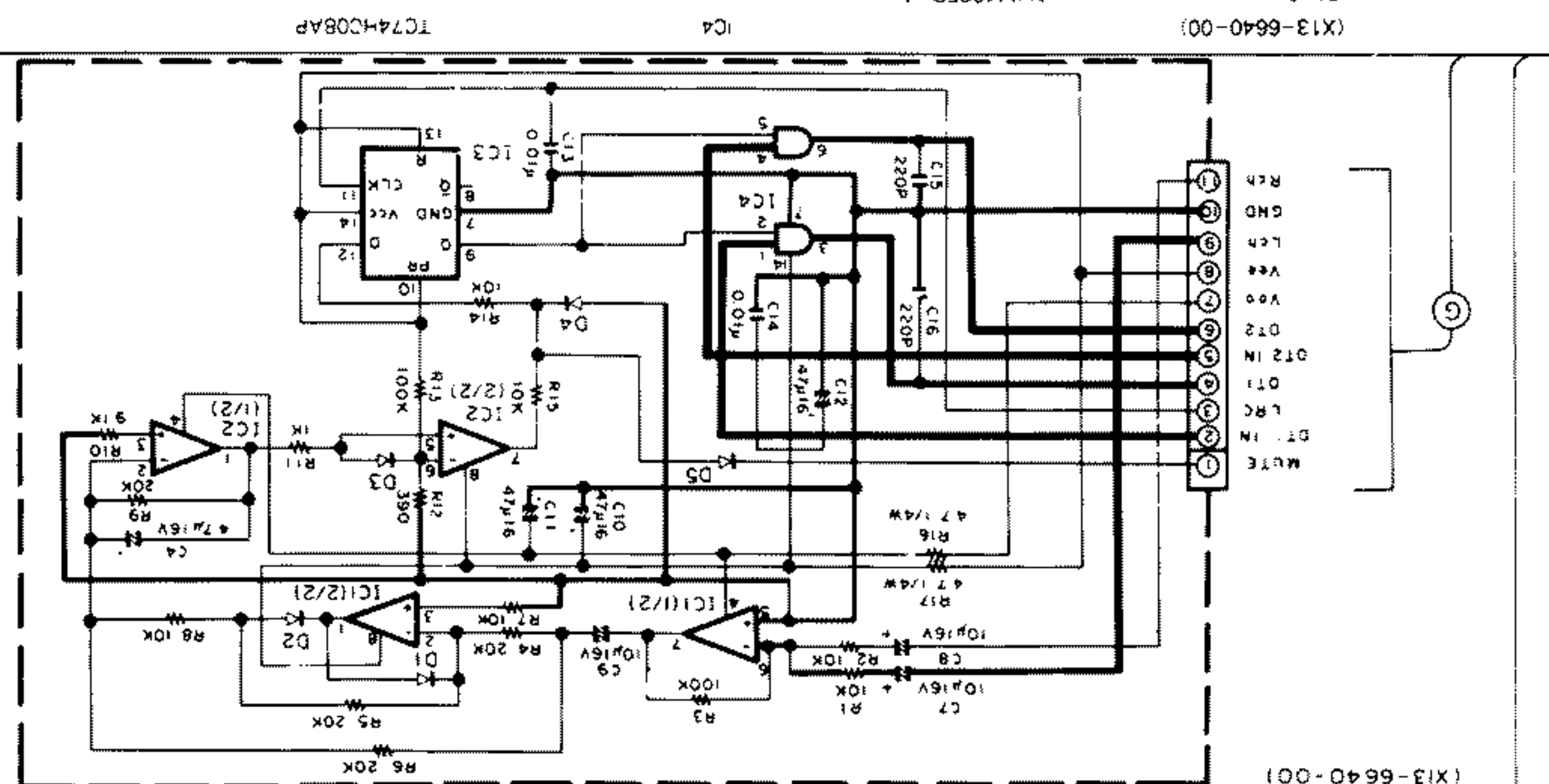
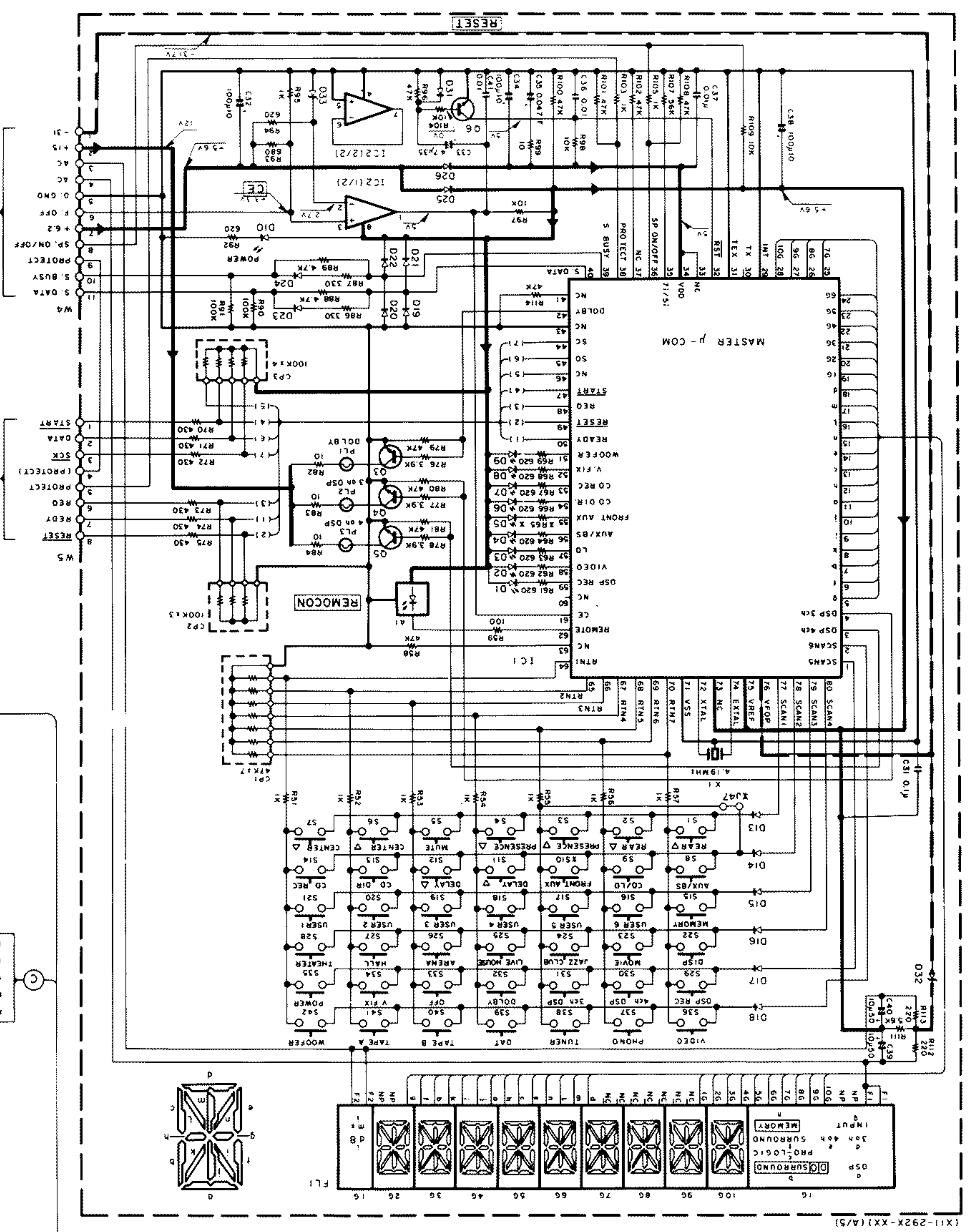
▲ Others
DC voltages are as measured with a high impedance voltmeter. Values may vary slightly due to variations between individual instruments or/and units.

Les tensions c.c. doivent être mesurées avec un voltmètre à haute impédance. Les valeurs peuvent différer légèrement du fait des variations inhérentes aux appareils et aux instruments de mesure individuels.

Die angegebenen Gleichspannungswerte wurden mit einem hochohmigen Spannungsmesser gemessen. Dabei schwanken die Meßwerte aufgrund von Unterschieden zwischen einzelnen Instrumenten oder Geräten u. U. geringfügig.

REF NO	CN9	10	CN1	12	CN15	16	D5	D27-31	E5	F1	F2	F3	J43	J44	J47	R37	R39	R39	R40	R85	R110	R125	126	S10	W6	WB
DESTINATION																										
M	YES	YES	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO
I	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO
X E	2-71	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO	NO

IC1	IC2	IC3	IC4	IC5
1	2	3	4	5
25C0003(L) K	AM6914 or BA10393	NJM4580-D or PC4570C-A	TAB4095	TA8409S
07.8	01-5.8.9	DB.7.10	D11.12	
25C2003(L) K	B30-1289-05	B30-1291-05	HSS131 or HSS104A	D03.34
25C7405(Q, R) or 25C945(A)(D, P)	D13-31	002	HSS11(B2) or R051E5(B2)	HSS27(N(B2) or R027E5(B2)



IC1 : CX50112-1540
 IC2 : AM6914 or BA10393
 IC3 5 : NJM4580-D or PC4570C-A
 IC4 : TAB4095
 D01.12
 D07.8
 D01-5.8.9
 D08.7.10
 B30-1289-05
 HSS11(B2) or R051E5(B2)
 HSS27(N(B2) or R027E5(B2)
 D03.34
 D13-31
 002
 HSS11(B2) or R051E5(B2)
 HSS131 or HSS104A
 25C7405(Q, R) or 25C945(A)(D, P)
 25C2003(L) K
 B30-1291-05
 B30-1289-05
 NJM4650-A
 TC74HC74AP
 NJM4650-A
 71-5
 15S133 or HSS104
 TC74HC08AP
 15S133 or HSS104

PARTS LIST

* New Parts

Parts without Parts No. are not supplied.

Les articles non mentionnés dans le Parts No. ne sont pas fournis.

Teile ohne Parts No. werden nicht geliefert.

Ref. No. 参照番号	Address 位置	New Parts	Parts No. 部品番号	Description 部品名/規格	Desti- nation 仕向	Re- marks 備考
IC1		*	CXP50112-154Q	IC		
IC2		*	AN6914	IC		
IC2			BA10393	IC(DUAL COMPALATOR)		
IC3			NJM4580D-D	IC		
IC3			UPC4570C-A	IC		
IC4			TAB409S	IC(MOTOR CONTROL)		
IC5			NJM4580D-D	IC		
IC5			UPC4570C-A	IC		
Q1 -6			2SC1740S(Q,R)	TRANSISTOR		
Q1 -6			2SC945(A)(Q,P)	TRANSISTOR		
Q7 ,8			2SC2003(L,K)	TRANSISTOR		
A1			W02-0776-05	ELECTRIC CIRCUIT MODULE		
A1		*	W02-1046-05	ELECTRIC CIRCUIT MODULE		
DSP SUB UNIT (X13-6640-00)						
C4			CE04JW1C4R7M	ELECTRO 4.7UF 16WV		
C7 -9			CE04JW1C100M	ELECTRO 10UF 16WV		
C10 -12			CE04JW1C470M	ELECTRO 47UF 16WV		
C13 ,14			C91-0769-05	CERAMIC 0.01UF K		
C15 ,16			CC45FSL1H221J	CERAMIC 220PF J		
D1 -5			HSS104	DIODE		
D1 -5			1SS133	DIODE		
IC1 ,2			NJM4580D-D	IC		
IC1 ,2			UPC4570C-A	IC		
IC3			TC74HC74AP	IC(DUAL D-TYPE FLIP FLOP)		
IC4			TC74HC08AP	IC		
SIGNAL PROCESSOR UNIT (X32-1710-00)						
C1 ,2			CE04KW1V100M	ELECTRO 10UF 35WV		
C3 ,4			CF92FV1H302J	MF 3000PF J		
C5 ,6			CF92FV1H102J	MF 1000PF J		
C7 ,8			CF92FV1H472J	MF 4700PF J		
C9 ,10			CK45FF1H103Z	CERAMIC 0.010UF Z		
C11 ,12			CF92FV1H471J	MF 470PF J		
C13 ,14			CF92FV1H562J	MF 5600PF J		
C15 ,16			CK45FF1H103Z	CERAMIC 0.010UF Z		
C17 ,18			CF92FV1H102J	MF 1000PF J		
C19 ,20			CF92FV1H184J	MF 0.18UF J		
C21 ,22			CK45FF1H103Z	CERAMIC 0.010UF Z		
C23 ,24			CC45FSL1H151J	CERAMIC 150PF J		
C25 ,26			CQ09FS1H182J	POLYSTY 1800PF J		
C27 ,28			CC45FSL1H180J	CERAMIC 18PF J		
C29 ,30			CK45FF1H473Z	CERAMIC 0.047UF Z		
C31 ,32			C90-1334-05	NP-ELEC 47UF 10WV		
C33 ,34			CK45FF1H103Z	CERAMIC 0.010UF Z		
C35 ,36			CE04KW1V100M	ELECTRO 10UF 35WV		
C37 ,38			CC45FSL1H101J	CERAMIC 100PF J		
C39 -42			CE04KW1V100M	ELECTRO 10UF 35WV		
C43 ,44			CF92FV1H022J	MF 8200PF J		
C45 ,46			CE04KW1C220M	ELECTRO 22UF 16WV		
C51 ,52			CK45FF1H103Z	CERAMIC 0.010UF Z		
C53			CE04KW1C220M	ELECTRO 22UF 16WV		
C54			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C55 -58			CE04KW1A101M	ELECTRO 100UF 10WV		
C59			CE04KW1A471M	ELECTRO 470UF 10WV		

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C60			CE04KW1C220M	ELECTRO 22UF 16WV		
C61			CK45FF1H473Z	CERAMIC 0.047UF Z		
C62			CE04KW1C220M	ELECTRO 22UF 16WV		
C63			CK45FF1H473Z	CERAMIC 0.047UF Z		
C64			CE04KW1C220M	ELECTRO 22UF 16WV		
C65			CK45FF1H473Z	CERAMIC 0.047UF Z		
C66			CC45FCH1H100D	CERAMIC 10PF D		
C67			CC45FCH1H330J	CERAMIC 33PF J		
C68			CK45FF1H473Z	CERAMIC 0.047UF Z		
C69			CK45FF1H103Z	CERAMIC 0.010UF Z		
C70			CE04KW0J331M	ELECTRO 330UF 6.3WV		
C71 ,72			CC45FSL1H101J	CERAMIC 100PF J		
C73 ,74			CC45FCH1H100D	CERAMIC 10PF D		
C75			CK45FF1H103Z	CERAMIC 0.010UF Z		
C76 ,77			CE04KW1V100M	ELECTRO 10UF 35WV		
C78			C90-1351-05	NP-ELEC 3.3UF 50WV		
C79 ,80			CK45FF1H103Z	CERAMIC 0.010UF Z		
C81 ,82			CE04KW1E470M	ELECTRO 47UF 25WV		
C83			CF92FV1H133J	MF 0.013UF J		
C84			CF92FV1H222J	MF 2200PF J		
C86			CE04DW0J102M	ELECTRO 1000UF 6.3WV		
C87 -89			CK45FF1H473Z	CERAMIC 0.047UF Z		
C90			CK45FF1H103Z	CERAMIC 0.010UF Z		
L1			L40-2281-17	SMALL FIXED INDUCTOR		
L2 -5			L40-3301-17	SMALL FIXED INDUCTOR(33UH,K)		
X1			L77-1125-05	CRYSTAL RESONATOR 24.576MHz		
X2			L77-1128-05	CRYSTAL RESONATOR 67.7376MHz		
R71 ,72			RD14GB2E150J	FL-PROOF RD 15 J 1/4W		
R111			RD14GB2E6R8J	FL-PROOF RD 6.8 J 1/4W		
VR1 ,2			R12-1086-05	TRIM POT. 1K		
D1 ,2			HSS104	DIODE		
D1 ,2			1SS133	DIODE		
D3			HZS5.6N(B2)	ZENER DIODE		
D3			RD5.6ES(B2)	ZENER DIODE		
D4 -6			HSS104	DIODE		
D4 -6			1SS133	DIODE		
IC1			NJM4558D	IC(OP AMP X2)		
IC2 -4			NJM4565D	IC(OP AMP X2)		
IC5		*	UPD6380GC-3BH	IC(DIGITAL SIGNAL PROCESSOR)		
IC6		*	HM50464RP-12	IC		
IC6		*	LM33464C-12	IC		
IC6		*	UPD41464CF-12	IC		
IC7			TC74HC74AP	IC(DUAL D-TYPE FLIP FLOP)		
IC8 ,9		*	KAS03	IC		
IC10		*	TD6726N	IC(16BIT A/D CONVERTER)		
IC11			NJM4565D	IC(OP AMP X2)		
IC12			NJM4558D	IC(OP AMP X2)		
Q1			2SD1266(Q,P)	TRANSISTOR		
Q2			2SB941(Q,P)	TRANSISTOR		
Q3			2SA733(A)(Q,P)	TRANSISTOR		
Q3			2SA933S(Q,R)	TRANSISTOR		
Q4			2SC1923(R,0)	TRANSISTOR		
Q7			2SD1302(S,T)	TRANSISTOR		
Q8			2SA733(A)(Q,P)	TRANSISTOR		

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参照番号	位置	新	部品番号	部品名 / 規格	仕向	備考
C35			C90-1827-05	BACKUP 0.047F 5.5WV		
C36 ,37			C91-0769-05	CERAMIC 0.01UF K		
C38			CE04JW1A101M	ELECTRO 100UF 10WV		
C39 ,40			CE04JW1H100M	ELECTRO 10UF 50WV		
C41			C91-0769-05	CERAMIC 0.01UF K		
C51 -54			CE04KW1E470M	ELECTRO 47UF 25WV		
C55			CE04KW1E101M	ELECTRO 100UF 25WV		
C56			C90-1333-05	NP-ELEC 22UF 10WV		
C57			C91-0971-05	FILM 0.01UF 250WV		
C58			CK45FF1H103Z	CERAMIC 0.010UF Z		
C59			C91-1421-05	FILM 0.01UF 250AC		
CN1			E08-0411-05	RECTANGULAR RECEPTACLE		
E5			E13-0311-05	PHONO JACK FRONT AUX	M	
F1			F05-4025-05	FUSE (SEMKO) (250V T4A)	M	
F1 ,2			F06-2021-05	FUSE (SEMKO) (250V T2A)	X	
F2			F06-2021-05	FUSE (SEMKO) (250V T2A)	TE	
F3			F06-2021-05	FUSE (SEMKO) (250V T2A)	M	
F4			F05-2525-05	FUSE (SEMKO) (250V T2.5A)	XE	
F4			F05-3121-05	FUSE (SEMKO) (250V T3.15A)		
CN11-14			J13-0075-05	FUSE CLIP	MT	
CN11,12			J13-0075-05	FUSE CLIP	XE	
CN15-18			J13-0075-05	FUSE CLIP	XE	
CN17,18			J13-0075-05	FUSE CLIP	MT	
X1			L78-0218-05	RESONATOR 4.19MHz		
CP1			R90-0816-05	MULTIPLE RESISTOR		
CP2			R90-0850-05	MULTIPLE RESISTOR		
CP3			R90-0482-05	MULTI-COMP 100KX4 J 1/6W		
R121,122			RD14GB2E121J	FL-PROOF RD 120 J 1/4W		
R123,124			RD14GB2E101J	FL-PROOF RD 100 J 1/4W		
R127,128			RD14GB2E100J	FL-PROOF RD 10 J 1/4W		
VR1		*	R29-5044-05	POTENTIOMETER MASTER VOLUME		
VR2 ,3		*	R06-3068-05	POTENTIOMETER BASS, TREBLE		
VR4		*	R01-5077-05	POTENTIOMETER BALANCE		
VR5		*	R01-5076-05	POTENTIOMETER INPUT BALANCE		
K1			S51-1052-05	MAGNETIC RELAY		
K2			S51-2094-05	MAGNETIC RELAY		
S1 -42			S40-1064-05	PUSH SWITCH	M	
S1 -9			S40-1064-05	PUSH SWITCH	XTE	
S11 -42			S40-1064-05	PUSH SWITCH	XTE	
D11 ,12			HSS104A	DIODE		
D11 ,12			ISS131	DIODE		
D13 -26			HSS104	DIODE	XTE	
D13 -26			ISS133	DIODE	XTE	
D13 -31			HSS104	DIODE	M	
D13 -31			ISS133	DIODE	M	
D31			HSS104	DIODE	XTE	
D31			ISS133	DIODE	XTE	
D32			HZ35.1N(B2)	ZENER DIODE		
D32			RD5.1ES(B2)	ZENER DIODE		
D33 ,34			HZ52.7N(B2)	ZENER DIODE		
D33 ,34			RD2.7ES(B2)	ZENER DIODE		
FL1		*	10-BT-68GK	FLUORESCENT INDICATOR TUBE		

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参照番号	位置	新	部品番号	部品名 / 規格	仕向	備考
Q8			2SA933S(Q,R)	TRANSISTOR		
Q9			2SD1302(S,T)	TRANSISTOR		

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C55 ,56			CK45FB1H332K	CERAMIC 3300PF K		
C57 ,58			CE04KW1V100M	ELECTRO 10UF 35WV		
C59 ,60			CK45FF1H103Z	CERAMIC 0.010UF Z		
C61 ,62			C91-0769-05	CERAMIC 0.01UF K		
C63 ,64			CE04KW1E470M	ELECTRO 47UF 25WV		
C65 ,66			CK45FB1H561K	CERAMIC 560PF K		
C67 -70			CE04KW1V100M	ELECTRO 10UF 35WV		
C71 ,72			CC45FSL1H470J	CERAMIC 47PF J		
C73 ,74			CE04KW1V100M	ELECTRO 10UF 35WV		
C75 ,76			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C77 -79			CK45FB1H102K	CERAMIC 0.001UF K		
C80 ,81			CE04KW1H4R7M	ELECTRO 4.7UF 50WV		
C82			CE04KW1C102M	ELECTRO 1000UF 16WV		
C83			CE04KW1H4R7M	ELECTRO 4.7UF 50WV		
C84			CE04KW1C102M	ELECTRO 1000UF 16WV		
C85			CE04KW1V100M	ELECTRO 10UF 35WV		
C86			CE04KW1H4R7M	ELECTRO 4.7UF 50WV		
C87			CE04KW1C471M	ELECTRO 470UF 16WV		
C89			CK45FF1H103Z	CERAMIC 0.010UF Z		
C130			CF92FV1H333J	MF 0.033UF J		
C131			CE04KW1E470M	ELECTRO 47UF 25WV		
C132			CF92FV1H334J	MF 0.33UF J		
C133			CF92FV1H104J	MF 0.10UF J		
C134			CF92FV1H105J	MF 1.0UF J		
C135			CE04KW1C220M	ELECTRO 22UF 16WV		
C136			CE04KW1C101M	ELECTRO 100UF 16WV		
C137			CF92FV1H273J	MF 0.027UF J		
C138			CF92FV1H822J	MF 8200PF J		
C139			CE04KW1H010M	ELECTRO 1.0UF 50WV		
C140			CE04KW1V100M	ELECTRO 10UF 35WV		
C141			CF92FV1H472J	MF 4700PF J		
C142			CE04KW1E470M	ELECTRO 47UF 25WV		
C143			CE04KW1V100M	ELECTRO 10UF 35WV		
C144,145			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C146			C91-0769-05	CERAMIC 0.01UF K		
C147			CE04KW1E470M	ELECTRO 47UF 25WV		
C148			CK45FF1H103Z	CERAMIC 0.010UF Z		
C151,152			CE04KW1V100M	ELECTRO 10UF 35WV		
C153			CE04KW1H2R2M	ELECTRO 2.2UF 50WV		
C154			CC45FSL1H101J	CERAMIC 100PF J		
C155-158			CK45FF1H223Z	CERAMIC 0.022UF Z		
C159			C91-0085-05	CERAMIC 0.022UF N		
C160			CK45FF1H103Z	CERAMIC 0.010UF Z		
E1			E13-0249-05	PHONO JACK PHONO		
E2			E13-0633-05	PHONO JACK CD/LD BS/AUX		
E3			E13-0445-05	PHONO JACK VIDEO/PLAY ADAPTER		
E4 ,5			E13-0633-05	PHONO JACK TUNER DAT TAPE		
E6			E13-0313-05	PHONO JACK LD/CD BS/AUX1 VIDE		
E7			E13-0297-05	PHONO JACK MONITOR		
L1			L40-1091-17	SMALL FIXED INDUCTOR		
X1		*	L78-0277-05	RESONATOR 12MHz		
CP1			R90-0493-05	MULTI-COMP 100KX9 J 1/6W		
CP2		*	R90-0864-05	MULTIPLE RESISTOR		

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CP3			R90-0859-05	MULTIPLE RESISTOR		
CP4			R90-0482-05	MULTI-COMP 100KX4 J 1/6W		
R59 ,60			RD14GB2E471J	FL-PROOF RD 470 J 1/4W		
R123			RD14GB2E100J	FL-PROOF RD 10 J 1/4W		
R125			RD14GB2E151J	FL-PROOF RD 150 J 1/4W		
R157			RS14KB3A390J	FL-PROOF RS 39 J 1W		
R203			RD14GB2E5R6J	FL-PROOF RD 5.6 J 1/4W		
S1			S31-2322-05	SLIDE SWITCH	M	
D1 -10			HSS104	DIODE		
D1 -10			ISS133	DIODE		
D15 ,16			HSS104	DIODE		
D15 ,16			ISS133	DIODE		
D19			HSS104	DIODE		
D19			ISS133	DIODE		
IC1			TC9164N	IC(16CH BILATERAL SELECTOR SW)		
IC2			TC9163N	IC(BILATERAL SWITCH X16)		
IC3			NJM45800-D	IC		
IC3			UPC4570C-A	IC		
IC4			TC9162N	IC(ANALOG SWITCH ARRAY)		
IC5			TC9215P	IC(ANALOG SWITCH X 6)		
IC7 ,8			NJM4565L	IC		
IC9			TC9213P	IC(2CH ELECTRONIC VOLUME)		
IC10			LA2730	IC(DOUBLE SYSTEM)		
IC11			NJM45800-D	IC		
IC11			UPC4570C-A	IC		
IC12		*	LA7951	IC(AV SELECTOR)		
IC15		*	UPD78214CV-668	IC		
IC16			NJM45800-D	IC		
IC16			UPC4570C-A	IC		
Q1			2SC2003(L,K)	IC TRANSISTOR		
CONTROL UNIT (X11-292X-XX)						
640	2A, 2B	*	A33-0116-04	REFLECTOR		
D1 -4			B30-1289-05	LED		
D1 -5			B30-1289-05	LED		
D6 ,7			B30-1291-05	LED		
D8 ,9			B30-1289-05	LED		
D10			B30-1291-05	LED		
PL1 -3		*	B30-1297-05	LAMP		
C1 ,2			CE04KW1H3R3M	ELECTRO 3.3UF 50WV		
C3 ,4			CC45FSL1H221J	CERAMIC 220PF J		
C5 ,6			CK45FB1H561K	CERAMIC 560PF K		
C7 ,8			CF92FV1H563J	MF 0.056UF J		
C9 ,10			CK45FB1H561K	CERAMIC 560PF K		
C11 ,12			CE04KW1V4R7M	ELECTRO 4.7UF 35WV		
C13 ,14			CE04KW1V100M	ELECTRO 10UF 35WV		
C15 ,16			CF92FV1H203J	MF 0.020UF J		
C17 -20			CE04KW1V100M	ELECTRO 10UF 35WV		
C21 ,22			CE04KW1A101M	ELECTRO 100UF 10WV		
C31			CF92FV1H104J	MF 0.10UF J		
C32			CE04JW1A101M	ELECTRO 100UF 10WV		
C33			CE04JW1V4R7M	ELECTRO 4.7UF 35WV		
C34			CE04JW1A101M	ELECTRO 100UF 10WV		

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